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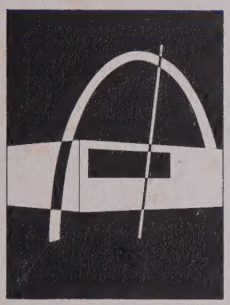

Digital Multimeter

PM2424

9447 024 240.1

9499 470 07301 731215

PHILIPS

PHILIPS

Manual

Digital multimeter PM2424

9447 024 240.1

PHILIPS

9499 470 07301

731215

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I. Introduction

GENERAL

The PM 2424 is a digital multimeter having a 4-digit display, capable of a maximum reading of 9999.
The instrument can measure:

- d.c. voltages of 100 µV to 1000 V
- a.c. voltages of 100 µV to 500 V rms
- d.c. currents of 100 nA to 1000 mA
- a.c. currents of 100 nA to 1000 mA rms
- Resistances of 100 mΩ to 10 MΩ

Protection for all measurement functions is provided.
The measuring range, the polarity for d.c. measurements and the position of the decimal point are all determined fully automatically. However, range and sample-hold facilities do exist.
Extremely high accuracies are obtained due to automatic - zero drift correction being carried out before each measurement.
The power consumption of the instrument is low due to the use of low-power and MOS-LSI digital integrated circuits.
Operation from mains or a d.c. supply is possible.
The d.c. supply provided by the optional accessory PM 9207 enables the portability of this instrument to be fully exploited.

II. Technical data

Properties expressed in numerical values with tolerances, are guaranteed by the manufacturer. Numerical values without tolerances serve only for information purposes and represent the properties of a typical instrument.

II-1. ELECTRICAL SPECIFICATIONS.

II-1.1. DC Voltage Measurement.

Range:	1/10/100/1000 V.
Ranging:	Fully automatic.
Max. sensitivity:	100 µV.
Resolution:	1/9999.
Input impedance:	More than 1000 MΩ for 10 V ranges, 10 MΩ for 100/1000 V ranges.
Accuracy:	± 0.01% of reading ± 0.01% at end-of-range ± 1 digit (at a temperature 23°C ± 2°C and humidity less than 85%).
Temperature coefficient:	0.005% of reading/°C at all ranges.
Stability (180 days):	0.02% of reading for 1 V range 0.01% of reading for 10 V range 0.02% of reading for 100/1000 V ranges
Offset current of input:	Less than 1 nA.
Protection:	1100 V max.
Response time:	0.2 sec. (without range changing)

II-1.2. AC Voltage Measurement.

Range:	1/10/100/500 V rms.
Ranging:	Fully automatic.
Max. sensitivity:	100 µV.
Resolution:	1/9999.
Input impedance:	1 MΩ shunted by 100 pF.
Accuracy:	± 0.3% of reading ± 0.2% at end-of-range ± 1 digit for 1/10/100 V ranges (frequency: 45 Hz to 20 kHz). ± 0.5% of reading ± 0.5% at end-of-range ± 1 digit for 1/10/100 V ranges (frequency: 40 Hz to 50 kHz). ± 0.5% of reading ± 0.5% at end-of-range ± 1 digit for 500 V range (frequency: 45 Hz to 10 kHz). (at a temperature 23°C ± 2°C and humidity less than 85%).

Temperature coefficient:	0.02% of reading/°C.
Stability (180 days):	0.05% of reading.
Protection:	500 V rms max.
Response time:	2.5 sec. (without range changing).

II-1.3. DC Current Measurement.

Range:	1/10/100/1000 mA.
Ranging:	Fully automatic.
Max. sensitivity:	100 nA.
Resolution:	1/9999.
Accuracy:	± 0.2% of reading ± 0.01% at end-of-range ± 1 digit for 1/10/100 mA ranges. ± 0.3% of reading ± 0.01% at end-of-range ± 1 digit for 1000 mA range. (at a temperature 23°C ± 2°C and humidity less than 85%).
Temperature coefficient:	0.01% of reading/°C.
Stability (180 days):	0.02% of reading.
Protection:	3A max.
Response time:	0.2 sec. (without range changing).

II-1.4. AC Current Measurement.

Range:	1/10/100/1000 mA.
Ranging:	Fully automatic.
Max. sensitivity:	100 nA.
Resolution:	1/9999.
Accuracy:	± 0.5% of reading ± 0.2% at end-of-range ± 1 digit for 1/10/100 mA ranges. ± 0.7% of reading ± 0.2% at end-of-range ± 1 digit for 1000 mA range (at a temperature 23°C ± 2°C and humidity less than 85%).
Temperature coefficient:	0.02% of reading/°C.
Stability (180 days):	0.05% of reading.
Protection:	3A max.
Response time:	2.5 sec. (without range changing).

II-1.5 Resistance Measurement.

Range:	1/10/100/1000/10000 kΩ.
Ranging:	Fully automatic.
Max. sensitivity:	100 mΩ.
Resolution:	1/9999.

Accuracy:	± 0.1% of reading ± 0.01% at end-of-range ± 1 digit for 1/10 kΩ ranges. ± 0.2% of reading ± 0.01% at end-of-range ± 1 digit for 100 kΩ range. ± 0.8% of reading ± 0.02% at end-of-range ± 1 digit for 1000 kΩ range. ± 1.5% of reading ± 0.05% at end-of-range ± 1 digit for 10000 kΩ range (at a temperature 23°C ± 2°C and humidity less than 85%).
Temperature coefficient:	0.015% of reading/°C for 1/10/100 kΩ ranges. 0.03% of reading/°C for 1000 kΩ range. 0.07% of reading/°C for 10000 kΩ range.
Stability (180 days):	0.03% of reading for 1/10/100 kΩ ranges. 0.05% of reading for 1000 kΩ range. 0.1% of reading for 10000 kΩ range.
Protection:	100 V max. in a.c./d.c. for continuous input. 200 V max. in a.c./d.c. for 5 sec. input.
Response time:	0.2 sec. (without range changing).

II-2. GENERAL DATA.

Type of operation:	Dual-slope integration.
Input terminal:	Completely floating.
Sampling speed:	2-5 samples/sec.
Ranging time:	200 ms-300 ms/range.
Polarity display:	+ or - can be automatically displayed.
Isolation:	600 V d.c. between chassis and black terminal (L0).
Display hold:	Measured result and range can be held.
CMRR (input):	50 dB (mains operation).
SMRR (analogue-to-digital converter):	40 dB (mains operation).
Power requirement:	220 V ± 10%, 50/60 Hz. Note: Mains transformer wiring can be altered for other mains voltages.
DC operation:	External + 12 V d.c. supply. Operation possible from + 10 V to + 15 V.
Power consumption:	Approx. 9 VA on mains operation. Approx. 3.5 VA on d.c. supply operation.

II-3. MECHANICAL DATA.

Dimensions:	Width 200 mm Height 72 mm Depth 253 mm.
Weight:	Approx. 2.1 kg.

III. Accessories

III-1. SUPPLIED WITH THE INSTRUMENT.

- mains input cable with connectors
- grounding lead
- measuring leads
- two shorting-plugs for range and sample-hold functions
- mating connector for the external d.c. supply input
- fuse, 50 mA, slow-blow
- manual.

III-2. OPTIONAL.

- d.c. power supply PM 9207.

  Mechanically, this power supply (Fig. 1) consists of an aluminium metal case designed to fit under the digital multimeter PM 2424 in place of the base plate. It houses a battery pack which contains 10 x 1.5 V nickel-cadmium rechargeable batteries. It also houses circuitry (Fig. 30) which enables a low-voltage indication to be generated within the display and a charging circuit which allows the batteries to be recharged via the mains supply to the multimeter.
  Electrical connections to the multimeter circuitry are made via the free connector (BU363) plugging into the fixed connector (BU364) on the underside of the printed circuit board U2.
  An external terminal on the case allows the condition of the batteries to be checked with ease.

  Dimensions:

Width	160 mm	approximate.
Height	30 mm	
Depth	210 mm	
Weight	1 kg	

Notes:

- R25 of the low-voltage indicator circuit should be adjusted so that LOW BATT indication is made in the display when the voltage of the battery pack falls below + 10 V d.c.
- The charging circuit for the batteries of the battery pack operates when the multimeter is connected to the mains supply irrespective of the settings of the power and power selector switches. Therefore, it is IMPORTANT that mains is only connected to the multimeter when the battery pack contains rechargeable batteries.

IV. Principle of operation

A simple block diagram of the instrument is shown in Fig. 2.

IV-1. ANALOGUE SECTION.

The analogue section consists of an input circuitry section and an analogue-to-digital converter (ADC).

IV-1.1. Input circuitry.

The function selector selects the required block of this circuitry depending upon the type of measurement being made. All input signals are converted to a d.c. voltage by the input circuitry.

- d.c. voltage.
  The input is applied to the DCV/ATT. section which attenuates the unknown d.c. voltage in accordance with the range.

- a.c. voltage.
  The input is applied to the ACV/DCV section and is attenuated, rectified and filtered to a d.c. voltage.

- d.c. current.
  The input is applied to the I/V section, where the unknown current is passed through a shunt resistor, which differs with each range. The voltage drop across this resistor is then measured and applied directly to the ranging amplifier of the analogue-to-digital converter.

- a.c. current.
  As d.c. current measurement with the exception that the output of the I/V section is applied to the input of the ACV/DCV section and not directly to the ranging amplifier.

- resistance (Fig. 3).
  Fixed current is passed through the unknown resistance Rx and the voltage across Rx is applied to the input of the ranging amplifier.
  This principle can be represented by the following equations.

  $Vx = Is.Rx$

  but

  $$Is = \frac{-V_{REF} - (-Vo)}{Rs} = -\frac{(V_{REF} - Vo)}{Rs}$$

  where

  Is: constant current

  Rs: standard resistance

  $-V_{REF}$: negative reference voltage

-Vo: voltage derived from $-V_{REF}$

Rx: unknown resistance

Vx: voltage across Rx.

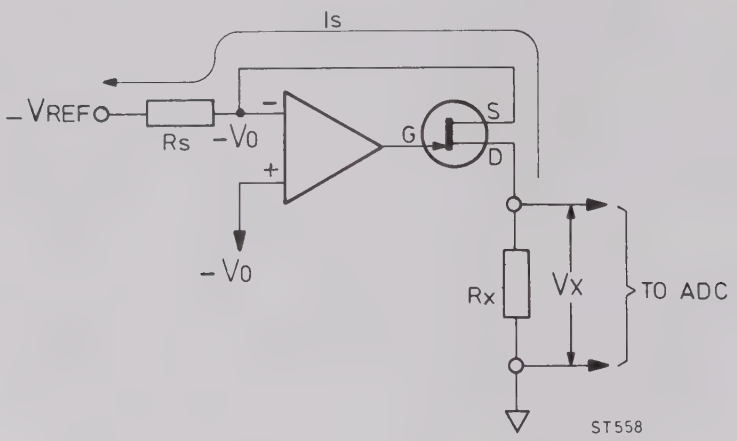


Fig. 3. Principle of resistance measurement.

IV-1.2. Analogue-to-Digital Converter (ADC).

This instrument employs a double-integrating system in the ADC and has an automatic-zero correction period.
The d.c. voltage from the input circuitry is applied to the ranging amplifier, where it is amplified in order to provide optimum range setting of the instrument.
The output of the ranging amplifier is applied to the integrator. A schematic diagram of the integrator is shown in Fig. 4.

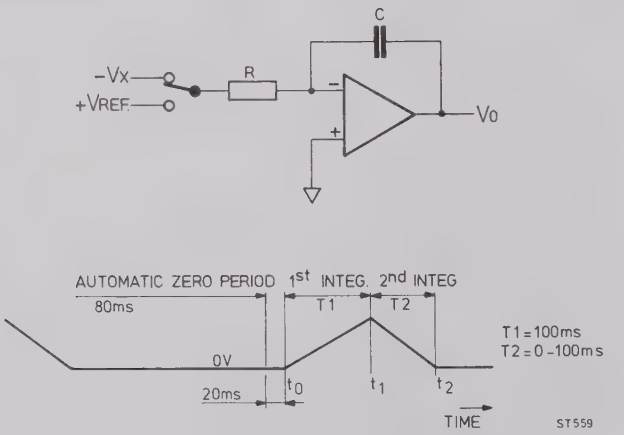


Fig. 4. Analogue-to-digital conversion system.

During the reference time ($t_o \rightarrow t_1$), -Vx, the input voltage is integrated. At the end of the 1st integration, the input to the integrator is switched to a reference voltage of opposite polarity to that of the input. This reference voltage is integrated and a waveform having a slope in the zero-level direction is produced at the output of the integrator.

A comparator detects the crossing of the zero-level by the output voltage Vo and ends the 2nd integration operation.
The magnitude of the input signal is obtained by measuring the duration of this 2nd integration.

The above operation is represented by the following equations:

$$Vo = \frac{1}{c} \int_{t_o}^{t_1} \frac{(-Vx)}{R} \cdot dt \qquad + \frac{1}{c} \int_{t_o}^{t_2} \frac{(V_{REF})}{R} dt, \quad = 0$$

but

$$t_1 - t_o = T1 \ , \ t_2 - t_1 = T2$$

$$\therefore Vo = \frac{T1}{CR} \cdot (-Vx) + \frac{T2}{CR} \cdot V_{REF} \ , = 0$$

$$\therefore \frac{T1}{CR} \cdot Vx = \frac{T2}{CR} \cdot V_{REF}$$

$$\therefore T2 = \frac{T1}{V_{REF}} \cdot Vx$$

but

T1 and V_{REF} are known and constant.

$$\therefore T2 = KVx.$$

The automatic-zero correction period precedes the 1st integration and the offset voltage of the ranging amplifier, integrating amplifier and comparator are stored.
The timing of the system is controlled by the digital section.

IV-2. DIGITAL.

IV-2.1. Display and logic section.

IV-2.1.1. Control circuitry.

The control signals are derived from the counter and the comparator.
The control circuitry is used to:

- generate the 1st integration period
- inhibit the clock at the end of the 1st integration
- generate the memory pulse at the end of the 2nd integration
- select the required polarity reference voltage for the 2nd integration
- generate the auto-zero correction period

- generate the settling times
- generate the DOWN and UP signals.

IV-2.1.2. Memory Pulse.

This pulse is generated at the end of the 2nd integration. It inhibits the clock and loads the contents of the counter into the latch.

IV-2.1.3. Counter and Latch.

The counter consists of four BCD-decade counters and counts the clock pulses (100 kHz). The contents of the four decade counters are supplied to the display via a latch on the occurrence of the memory pulse.

IV-2.1.4. Sample Hold.

The generation of the memory pulse is inhibited when the sample-hold condition is activated. Therefore, as the memory pulse acts as the transfer pulse between the counter and the latch, the display will be held.

IV-2.1.5. Over.

If the memory pulse has not been generated within 100 ms after the start of the 2nd integration then the OVER signal will be generated. When this occurs in the range-hold condition, the OVER indicator is permanently lit.

IV-2.1.6. Polarity.

The polarity is determined at the start of the 2nd integration and is indicated at the display after the occurrence of the memory pulse.

IV-2.2. Autorange Section.

IV-2.2.1. Autorange and Relay Drive.

The automatic ranging selects and sets the optimum range in accordance with the magnitude of the input signal. Control functions for the FET and relay switches of the input circuitry are generated. Also decode for the indication of the appropriate decimal point in the display is produced.

IV-2.2.2. Range Hold.

When the range-hold condition is activated, the autoranging is inhibited and the decimal point position in the display remains fixed.

V. Installation

V-1. POWER SOURCE.

V-1.1. AC supply.

The instrument is wired for operation from a 220 V, 50/60 Hz mains supply, but by altering the wiring to the mains transformer (refer Fig. 31) other mains voltages can be used. The cabinet of the instrument is earthed via the mains lead.

V-1.2. DC supply.

Operation from an external d.c. supply of between + 10 V and + 15 V is possible. The optional accessory PM 9207 is recommended for this mode of supply as it becomes an integral part of the instrument. The cabinet of the instrument may be earthed via a terminal on the rear panel.

V-2. CONTROLS AND CONNECTORS.

Front Panel Fig. 5.		
No.	Name	Function
1	Numerical Display Tubes	4 digit numerical display. Maximum display 9999. Decimal Point is automatically positioned.
2	Polarity Indicator	+ or - automatically indicated in accordance with polarity of input signal for d.c. voltage and d.c. current measurement.
3	OVER Indicator	Indicates that the input signal has exceeded the measurement range.
4	LOW BATT. Indicator	Indicates that the voltage of the battery pack (of the optional accessory PM 9207) is between + 5 V and + 10 V d.c. and unsuitable for satisfactory operation of the instrument.
5	Function Selector	Selects the required measurement function.
	VDC	d.c. voltage measurement.
	VAC	a.c. voltage measurement.
	mADC	d.c. current measurement.
	mAAC	a.c. current measurement.
	kΩ	Resistance measurement.

Right side panel Fig. 6		
No.	Name	Function
6	POWER switch	ON/OFF switch for both mains and d.c. supply operation
7	INPUT terminals	Input signal is connected to these terminals via input cable. Red terminal is HI Black terminal is LO.
8	SAMPLE HOLD terminals	By shorting with the two-pin accessory plug, the display is held until the short is removed.
9	RANGE HOLD terminals	By shorting with the two-pin accessory plug, the decimal point position is held until the short is removed.

Rear panel Fig. 7		
No.	Name	Function
10	a.c. power connector	Mains input via the accessory mains cable.
11	AC-DC power source selector switch	Selects between the a.c. and d.c. power sources.
12	EXT.DC + 12 V	External d.c. power source input. Note: Input for the d.c. power supply (optional accessory PM 9207) is not made via this connector.
13	Ground terminal	This terminal can be used to connect the cabinet to earth when operating from d.c. power source.
14	Fuse	Mains supply fuse of 50 mA rating (slow-blow).
15	PROTECTOR	Overcurrent protection in current measurement mode. To resume measurement, the cut-out button is depressed.

VI. Operating instructions

Note: It is important that the calibration procedure (as described in the Service Data) has been carried out within 6 months prior to the use of the instrument.

VI-1. SWITCHING-ON.

The instrument can be operated from an a.c. or d.c. power source.

VI-1.1. a.c. source.

- Set the AC-DC power selector switch to AC.
- Connect the instrument to the mains supply via the accessory mains cable.
- Set the POWER switch to ON.

VI-1.2. d.c. source.

It is recommended that the optional accessory PM 9207 d.c. power supply is used. This is fitted to the instrument in place of the base plate. The free connector of this accessory must be plugged into the fixed connector protruding from the under-side of the printed circuit board U2 before the accessory case is secured to the instrument. If an external d.c. supply is used, this is connected to the EXT. DC + 12 V connector at the rear of the instrument (the central pin of this connector is negative).

- Set AC-DC power selector to DC
- Set the POWER switch to ON.

Note: It is advisable to check the voltage of the d.c. supply (refer V.2). The optional PM 9207 has an external terminal for this purpose. Also, the LOW BATT indicator of the display is functional when using this accessory and will be lit if the battery voltage is inadequate (refer III.2).

VI-2. MEASURING.

The function required is set at the function selector.

VDC: 100 µV to 1000 V d.c.

VAC: 100 µV to 500 V r.m.s.

mADC: 100 nA to 1000 mA d.c.

mAAC: 100 nA to 1000 mA r.m.s.

kΩ: 0.1 Ω to 10 MΩ.

VI-2.1. d.c. voltages.

- Depress the switch VDC.
- Connect the voltage source to the INPUT terminals via the input cable.

Notes:

- The maximum permissible input is 1100 V.
- Maximum permissible voltage between the black (LO) input terminal and chassis is 600 V.
- Polarity indication is made.
- OVER range indication is not made for input voltages exceeding 1000 V d.c.

VI-2.2. a.c. voltages.

- Depress the switch VAC.
- Connect the voltage source to the INPUT terminals via the input cable.

Notes:

- OVER range indication is not made for input voltages exceeding 500 V r.m.s.

VI-2.3. d.c. currents.

- Depress the switch mADC.
- Connect the current circuit to the INPUT terminals via the input cable.

Notes:

- Maximum permissible input current is 3A.
- Maximum measurable input current is 1A and an overcurrent protector is actuated for input currents exceeding 1.6A, disconnecting the measuring circuit. Measurement can be resumed by pushing the protector button on the rear panel.
- OVER range indication is made for currents exceeding 1A, unless the protector has been actuated.
- Polarity indication is made.

VI-2.4. a.c. currents.

- Depress the switch mAAC.
- Connect the current circuit to the INPUT terminals via the input cable.

Note: As 2.3., but there is no polarity indication for a.c. currents.

VI-2.5. Resistance.

- Depress the switch kΩ.
- Connect the unknown resistance to the input terminals via the input cable.

Notes:

- Maximum permissible voltage at the input terminals when measuring resistance is 100 V (continuous) or 200 V (5 sec. duration).

- In a noisy environment, particularly when measuring resistance, the display may vary considerable. If this does occur, connect the (LO) input terminal to the ground (chassis) terminal. Otherwise, place the unknown resistance inside a shielded box.

VI-3. GENERAL NOTES.

VI-3.1. Polarity indication.

For d.c. measurements (VDC, mADC) the polarity of the input signal is indicated to the left of the display. The indicated polarity is that of the red (HI) input terminal with respect to the black (LO) input terminal.

VI-3.2. Range-hold.

When the RANGE-HOLD terminals are shorted, the range, prior to applying the short, is held and the position of the decimal point is fixed. Automatic ranging has been inhibited.

Example.

Input	Display	Range hold terminals
0 V	.0000	Open
+ 19.19 V	+ 19.19	Open
+ 19.19 V	+ 19.19	Shorted
0 V	00.00	Shorted

VI-3.3. Sample-hold.

When the SAMPLE-HOLD terminals are shorted the complete display, prior to applying the short, is held.
It remains held until the short is removed.

VI-3.4. Overrange indication.

The OVER indicator is situated above the polarity indicator and is lit when:

- The input signal exceeds the held measurement range.
- Set to kΩ with the INPUT terminals open and autoranging.

I. Einleitung ALLGEMEINES

Das PM 2424 ist ein digitales Vielfachmessgerät mit 4-stelliger Anzeige; ihr höchster Ziffernwert ist: 9999.
Das Gerät hat folgende Messbereiche:

- Gleichspannung : 100 µV bis 1000 V
- Wechselspannung (effektiv) : 100 µV bis 500 V eff.
- Gleichstrom : 100 nA bis 1000 mA
- Wechselstrom (effektiv) : 100 nA bis 1000 mA eff.
- Widerstand : 100 mΩ bis 10 MΩ

Für alle Messfunktionen ist ein Ueberlastungsschutz eingebaut.
Messbereich, Polarität bei Gleichstrom-/Gleichspannungsmessungen und Kommaposition werden vollautomatisch bestimmt. Ausserdem können angezeigter Bereich und Messwertanzeige festgehalten werden.
Automatische Nullpunkt-Konpensation und -Korrektur vor jeder Messung garantieren eine extrem hohe Genauigkeit.
Durch den Einsatz von Kleinleistungs- und MOS-LSI Festkörperschaltkreisen wird ein geringer Eigenstromverbrauch des Gerätes erreicht.
Sowohl Netz- als auch Batteriebetrieb sind möglich.
Dazu kann auf Wunsch der Zusatz PM9207 geliefert werden, der das Gerät vom Netz völlig unabhängig macht.

V. Installation

V-1. ALIMENTATION.

V-1.1. Alimentation alternative.

L'appareil est conçu pour l'usage en 220 V, 50/60 Hz alimentation secteur. Cependant, il peut être utilisé à d'autres tensions secteur en modifiant le câblage du transformateur secteur (voir Fig. 31).

V-1.2. Alimentation continue.

Le fonctionnement à partir d'une alimentation continue externe entre + 10 V et + 15 V est possible. L'accessoire en option PM 9207 est recommandé pour ce mode d'alimentation, car il fail alors partie intégrale de l'appareil. Le coffret de l'appareil peut être mis à la terre par l'intermédiaire d'une borne sur le panneau arrière.

V-2. ORGANES DE REGLAGE ET CONNECTEURS;

Panneau avant Fig. 5

No	Désignation	Fonction
1	Tubes d'affichage Numérique	affichage numérique à 4 chiffres affichage maximal 9999 point décimal automatiquement positionné
2	Indicateur de polarité	+ ou - automatiquement indiqué en fonction de la polarité du signal d'entrée pour mesure de tension continue et courant continu.
3	Indicateur "OVER"	Indique que le signal d'entrée a dépassé la gamme de mesure.
4	Indicateur "LOW BATT"	Indique que la tension du jeu de batteries (de l'accessoire en option PM9207) est situé entre +5 V et +10 V continu et insuffisante pour obtenir un fonctionnement satisfaisant de l'appareil.
5	Sélecteur de fonction	Sélectionne la fonction de mesure requise
	VDC	mesure de tension continue
	VAC	mesure de tension alternative
	mADC	mesure de courant continu
	maAC	mesure de courant alternatif
	kΩ	mesure de résistance

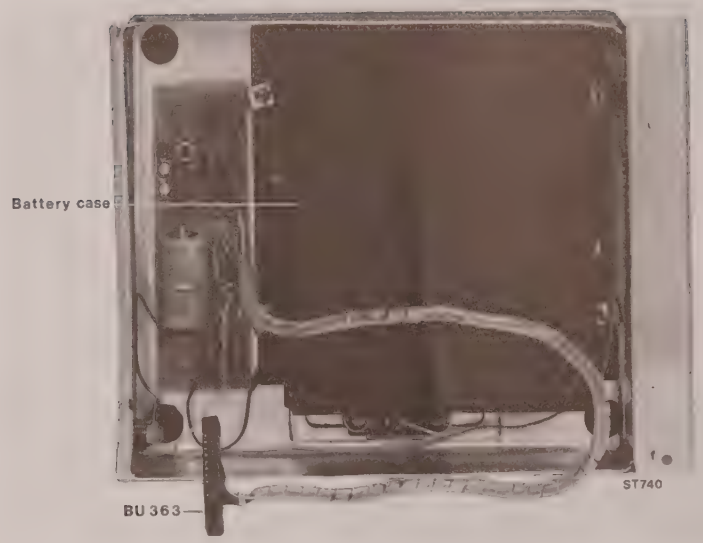


Fig. 1.1

Fig. 1.2

Panneau de droite Fig. 6		
No.	Désignation	Fonction
6	commutateur POWER	commutateur ON/OFF pour fonctionnement en secteur ou en alimentation continue
7	Bornes INPUT	signal d'entrée est connecté à ces bornes par l'intermédiaire du câble d'entrée Borne rouge HI Borne noire LO
8	Bornes SAMPLE HOLD	En cout-circuitant avec la fiche accessoire à deux broches, l'affichage est retenu jusqu'à ce que le court-circuit soit éliminé.
9	Bornes RANGE HOLD	En court-circuitant la fiche accessoire à deux broches, la position du point décimal est maintenue jusqu'à ce que le court-circuit soit éliminé.

Panneau arrière Fig. 7		
No.	Désignation	Fonction
10	connecteur d'alimentation alternative	Entrée secteur par l'intermédiaire du câble accessoire secteur
11	Sélecteur de source de puissance AC-DC	Sélectionne entre sources de puissance alternative ou continue
12	EXT.DC + 12 V	Entrée de source continue externe Remarque· l'entrée pour l'alimentation continue accessoire en option PM 9207 n'est pas faite par ce connecteur.
13	Borne de terre	Cette borne peut être utilisée pour connecter en cas de le coffret à la terre lorsque le fonctionnement à partir d'une source d'alimentation continue.
14	fusible	fusible d'alimentation secteur de 50 mA (à action différée)
15	PROTECTOR	protection contre l'excès de courant en mode de mesure de courant. Pour reprendre la mesure, enfoncer le bouton d'interruption.

VI. Opération

Remarque· Il est important que le processus d'étalonnage (décrit dans la documentation de service) soit effectué dans les six mois précédant l'utilisation de l'appareil.

VI-1. ENCLENCHEMENT

L'appareil peut être alimenté par source continue ou alternative.

VI-1.1. Source alternative.

- Mettre le sélecteur AC-DC en position AC
- Connecter l'appareil à l'alimentation secteur par l'intermédiaire du cordon secteur accessoire.
- Mettre le commutateur POWER sur ON

VI-1.2. Source continue.

Il faut que l'accessoire en option PM 9207 (alimentation continue) soit utilisé. Celui-ci est monté en place de la plaque de base de l'appareil. Le connecteur libre de cet accessoire doit être enfiché dans le connecteur fixe en protubérance de la partie inférieure de la platine U2 avant de monter le boîtier d'accessoire sur l'appareil. Si l'alimentation continue externe est utilisée, elle est connectée au connecteur EXT.DC + 12 V à l'arrière de l'appareil (la broche centrale de ce connecteur est négative).

- Mettre le sélecteur AC-DC en position DC
- Mettre le commutateur POWER sur ON.

Remarque· Il est recommandé de contrôler la tension d'alimentation continue (voir V .2.). Le PM 9207 en option présente une borne externe à cet effet. De plus, l'indicateur LOW BATT de l'affichage est également fonctionnel en cas d'utilisation du présent accessoire et s'illume la tension de batterie est inadéquate (voir III.2).

VI-2 MESURE.

Le fonction requise est réglée à l'aide du sélecteur de fonction.

VDC: 100 µV à 1000 V continu

VAC: 100 µV à 500 V eff

mADC: 100 nA à 1000 mA continu

mAAC: 100 nA à 1000 mA eff

kΩ: 0.1 Ω à 10 MΩ

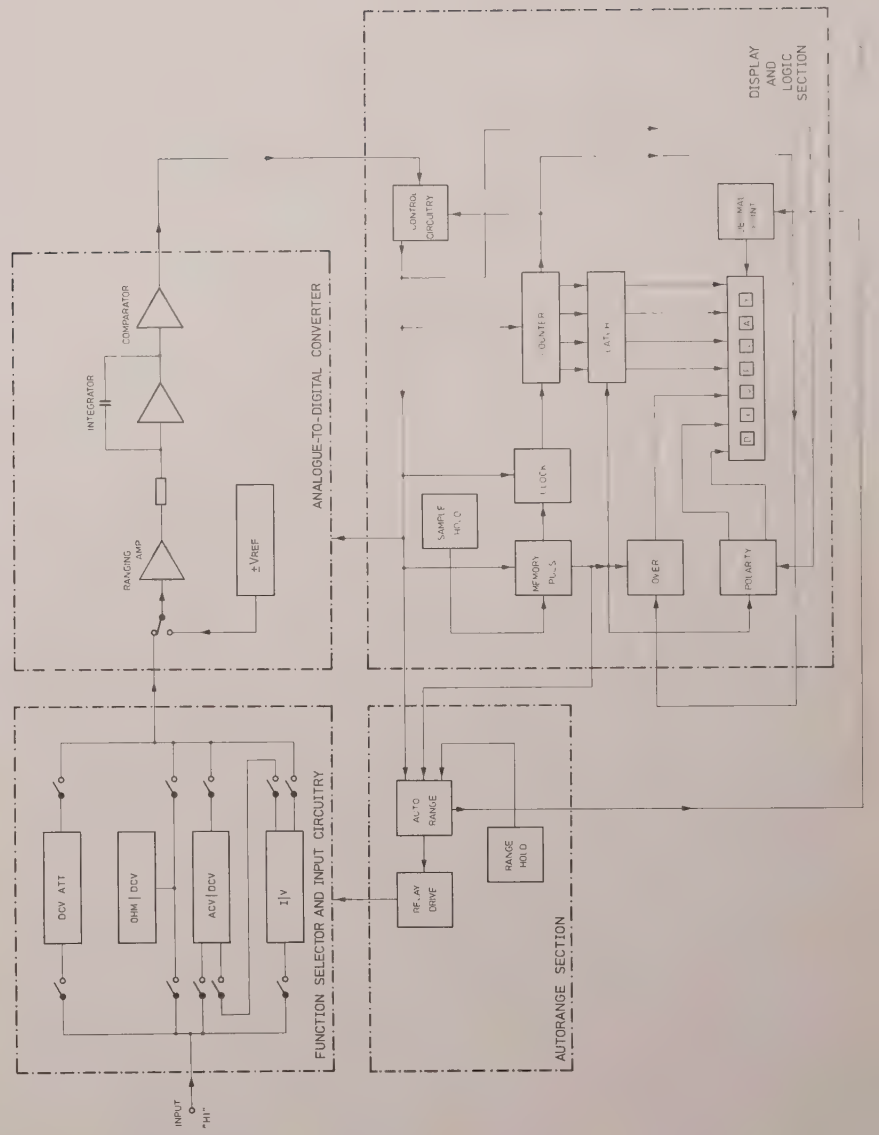


Fig. 2

VI-2.1. Tensions continues

- Enfoncer le commutateur VDC
- Connecter la source de tension aux bornes INPUT par l'intermédiaire du câble d'entrée

Remarques:

- L'entrée maximale admise est de 1100 V.
- La tension maximale admise entre borne noire (LO) et châssis est de 600 V.
- L'indication de polarité est présente
- L'indication de gamme OVER n'est pas présente pour des tensions d'entrée excédant 1000 V continu.

VI-2.2. Tensions alternatives.

- Enfoncer le commutateur VAC
- Connecter la source de tension aux bornes INPUT par l'intermédiaire du câble d'entrée.

Remarques:

- L'indication de gamme OVER n'est pas présente pour des tensions d'entrée excédant 500V

VI-2.3. Courants continus

- Enfoncer le commutateur mADC
- Connecter le circuit de courant aux bornes INPUT par l'intermédiaire du câble d'entrée.

Remarques:

- Le courant d'entrée maximal admis est de 3 A.
- Le courant d'entrée maximal mesurable est de 1 A et une protection de courant en excès est enclenchée pour des courants d'entrée excédant 1,6 A, déconnectant le circuit de mesure. La mesure peut être reprise en enfonçant le bouton PROTECTOR sur le panneau arrière.
- L'indication de gamme OVER est présente pour des courants excédant 1 A, à moins que le PROTECTOR soit actionné.
- L'indication de polarité est présente.

VI-2.4. Courants alternatifs

- Enfoncer le commutateur mAAC
- Connecter le circuit de courant aux bornes INPUT par l'intermédiaire du câble d'entrée.

Remarque: Tout comme pour 2.3. il n'y a pas d'indication de polarité.

VI-2.5. Résistance

- Enfoncer le commutateur kΩ
- Connecter la résistance inconnue aux bornes d'entrée par l'intermédiaire du câble d'entrée.

Remarques:

- La tension maximale admise aix bornes d'entrée est de 100 V (continuellement) ou 200 V (d'une durée de 5 secondes).

- Dans un environnement bruyant, particulièrement pour la mesure de résistance, l'affichage varie considérablement. Dans ce cas, connecter la borne d'entrée LO à la borne de terre (châssis). Autrement, placer la résistance inconnue dans une boîte blindée.

VI-3. INDICATIONS GENERALES

VI-3.1. Indication de polarité

Pour des mesures de tension ou courant continus (VDC, mADC), la polarité du signal d'entrée est indiquée à gauche de l'affichage. La polarité indiquée est celle de la borne d'entrée rouge (HI) par rapport à la borne d'entrée noire (LO).

VI-3.2. Maintien de gamme

Lorsque les bornes RANGE-HOLD sont court-circuitées, la gamme est maintenue avant de procéder au court-circuitage et la position du point décimal doit être fixée. La sélection de gamme automatique est inhibitée.

Exemple.

Entrée	Affichage	Bornes RANGE-HOLD
0 V	.0000	ouvertes
+ 19.19 V	+ 19.19	ouvertes
+ 19.19 V	+ 19.19	court-circuitées
0 V	00.00	court-circuitées

VI-3.3. Maintien d'échantillon

Lorsque les bornes SAMPLE-HOLD sont court-circuitées, l'affichage complet est maintenu avant de procéder au court-circuitage.
L'affichage reste maintenu jusqu'à ce que le court-circuit soit éliminé.

VI-3.4. Indication de gamme en excès.

L'indicateur OVER se trouve au-dessus de l'indicateur de polarité et s'allume lorsque·

- le signal d'entrée excède la gamme de mesure maintenue.
- l'appareil est réglée en kΩ avec bornes INPUT ouvertes et sélection de gamme automatique.

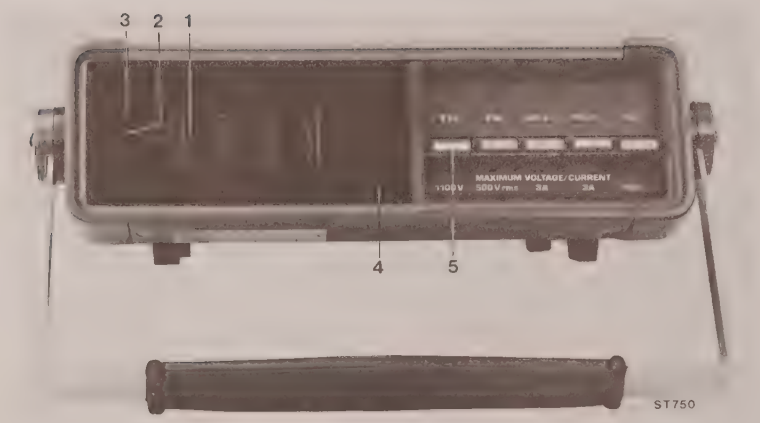


Fig. 5

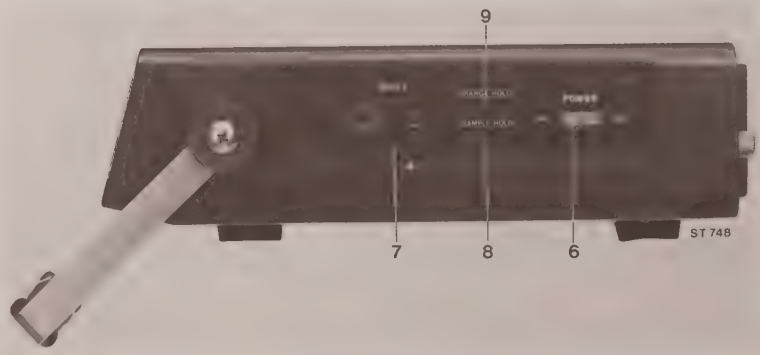


Fig. 6

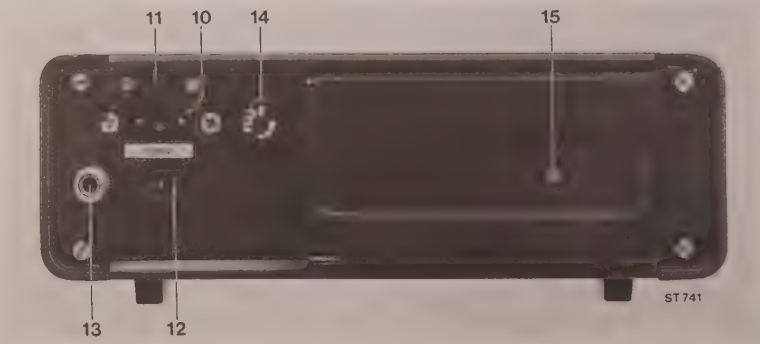


Fig. 7

A detailed block diagram is shown in Fig. 8.

VII-1. ANALOGUE SECTION.

Circuit diagrams of the input circuitry (Fig.41) and the analogue-to-digital converter (Fig.42) are given at the end of the manual.

VII-1.1. DC voltage measurement (Fig. 9).

The d.c. voltage input signal is applied to a d.c. voltage attenuation circuit via the function selector.
Input voltages greater than 10 V in magnitude are attenuated by 100.
The output is applied to the analogue-to-digital converter (ADC) via the function selector.
The neon tube LA 206 provides high voltage protection for the relay RE 192.
The combination of capacitor C 180 and resistor R 134 attenuates any spikes produced during switching.
The relays are drawn in the normal (unenergised) position and TABLE 1 gives the control functions which are generated by the autorange circuit (AR) of the digital section.

TABLE 1. RELAY STATES OF DCV ATTENUATION CIRCUIT.

Control	TS 28 (AR)	TS 29 (AR)	TS 29 (AR)	
Relay	RE 191	RE 192	RE 193	
Range	State	State	State	Gain
1 V	Energised			1
10 V	Energised			1
100 V		Energised	Energised	0.01
1000 V		Energised	Energised	0.01

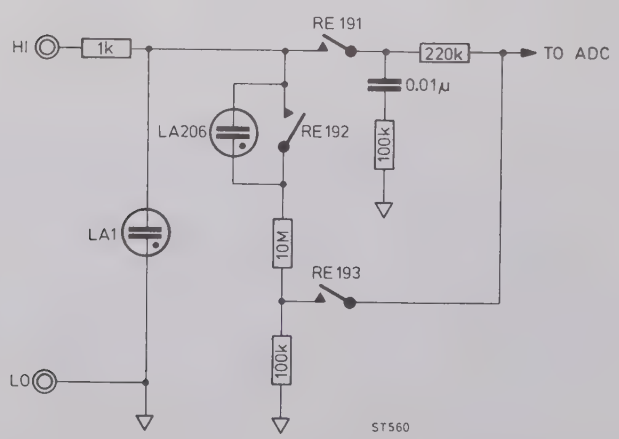


Fig. 9. DC voltage measurement.

VII-1.2. AC voltage measurement (Fig. 10).

The a.c. voltage input signal is switched to the input of the ACV/DCV converter by the function selector. This converter consists of the following circuits:

VII-1.2.1. Attenuator.

The attenuation is set by relays in accordance with the magnitude of the input signal (see Table 2).

VII-1.2.2. Buffer amplifier.

The buffer amplifier A1 has unity gain and consists of a source follower and an emitter follower.

VII-1.2.3. Rectifying amplifier and filter.

The rectifying amplifier employs a half-wave rectification system.

Conversion to a d.c. voltage equal to the r.m.s. value of the buffer amplifier output results by passing the half-wave rectified voltage through a two-stage filter. For r.m.s. voltages in excess of 100 V, RE 197 is energised and an attenuation of 10 is provided by the rectifying amplifier.

TABLE 2. RELAY STATES OF ACV/DCV CONVERTER CIRCUIT.

Control	TS 30 (AR)	TS 31 (AR)	TS 32 (AR)	TS 33 (AR)	
Relay	RE 194	RE 195	RE 196	RE 197	
Range	State	State	State	State	Gain
1 V r.m.s.	Energised				1
10 V r.m.s.		Energised			0.1
100 V r.m.s.		Energised	Energised		0.01
500 V r.m.s.		Energised	Energised	Energised	0.001

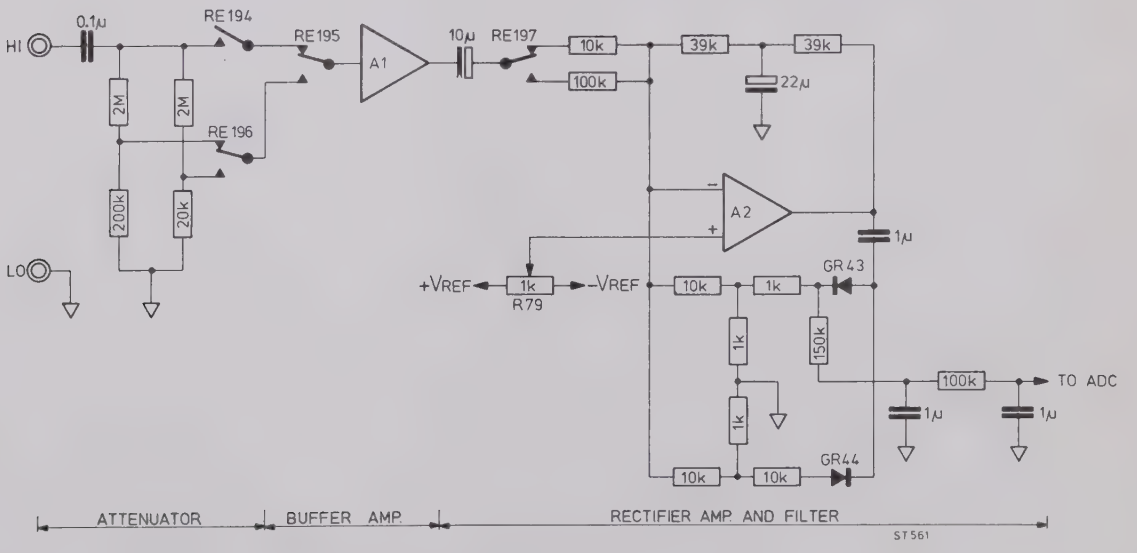


Fig. 10. AC voltage measurement

VII-1.3. AC and DC current measurement (Fig. 11).

The current flows into the I/V converter via the function selector. The shunt resistance is selected in accordance with the magnitude of the current so that a maximum voltage of 1 V is dropped across the shunt resistance.
In Fig. 11 the relays are drawn in the normal (unenergised) position and the range setting states of the relays are given in TABLE 3.
A protector is provided at the input to automatically block the input when an overcurrent exceeding 1.5 A d.c. (or 1 A r.m.s.) flows.
GR 53 and GR 54 act as spike-clamping diodes at the instant of range switching.
For d.c. current, the output of the I/V converter is applied to the ADC via the function selector. However, for a.c. current the output of the I/V converter is applied to the ACV/DCV converter via the function selector.

TABLE 3. RELAY STATES OF THE I/V CONVERTER CIRCUIT.

Control	TS 34 (AR)	TS 35/36 (AR)	
Relay	RE 198	RE 199	
Range	State	State	Shunt resistor
1 mA			1 kΩ
10 mA		Energised	100 Ω
100 mA	Energised		10 Ω
1000 mA	Energised	Energised	1 Ω

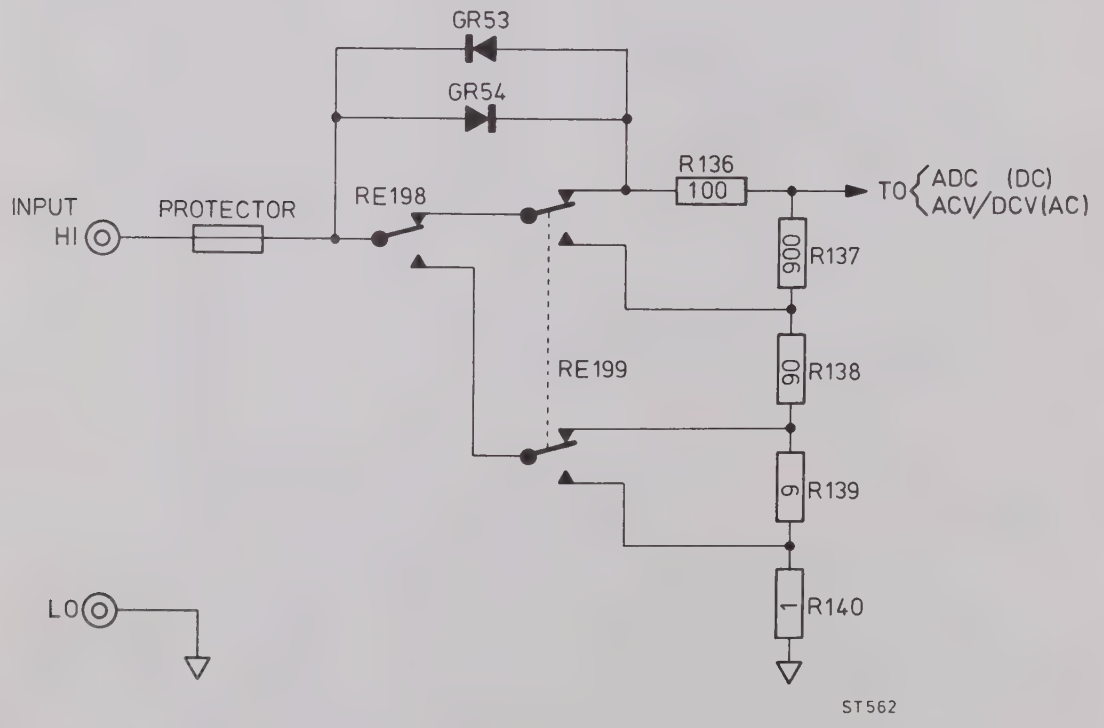


Fig. 11. Current measurement.

VII-1.4. Resistance measurement (Fig. 12).

A fixed source current which differs for each range, is passed through the unknown resistance, Rx, from the OHM/DCV converter via the function selector. The voltage developed across the resistor is detected by the ADC, again via the function selector. The magnitude of the source current is determined by the source resistance, Rs, which is selected by solid-state (FET) switches. The source current is set so that the maximum voltage dropped across the unknown resistance is 1 V. The operating states of the range-switching FET switches are given in TABLE 4. Resistor R 106 and diodes GR 55 and GR 56 provide a protection circuit against voltages being accidently applied to the input terminals when the instrument is set to measure resistance.

TABLE 4. SOURCE CURRENT SELECTION FOR RESISTANCE MEASUREMENT.

Control	IC 8/8	IC 11/11	IC 11/8	IC 9/8	
FET SWITCH	TS 20	TS 21	TS22	TS 23	
Range	State	State	State	State	Is (μA)
1 kΩ	OFF	OFF	OFF	ON	1000
10 kΩ	OFF	OFF	ON	OFF	100
100 kΩ	OFF	ON	OFF	OFF	10
1 MΩ	ON	OFF	OFF	OFF	1
10 MΩ	OFF	OFF	OFF	OFF	0.1

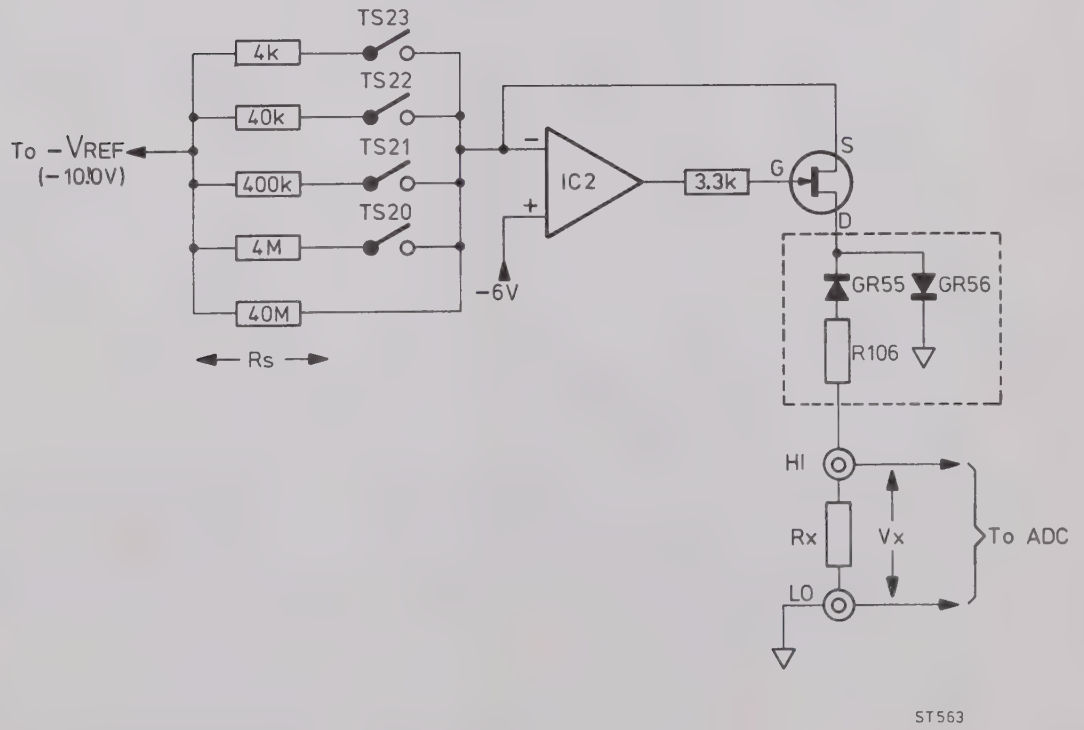


Fig. 12. Resistance measurement.

VII-1.5. Analogue-to-Digital Converter (Fig. 33).

A schematic diagram of the ADC is shown in Fig. 13.
The sequence diagram Fig. 14 is to be read in conjunction with Fig. 13.
The d.c. signal from the input circuitry (± 10 V maximum) is applied to the input of the ADC via the function selector.

Note: If voltages exceeding ± 10 V are applied to the input of the ADC, limiting is provided by TS 26, TS 28 (- 10 V) and TS 27, TS 29 (+ 10 V).

The conversion system of the ADC uses the dual-slope principle.
The conversion time can be considered as 5 distinct periods:

- 1st integration of duration 100 ms.
- 2nd settling time of duration 100 µs.
- 2nd integration. The duration of this period is directly related to the magnitude of the input voltage and a the end-of-range value is 100 ms.
- Auto-zero correction time of duration 80 ms. This period starts 200 ms after the start of the 2nd integration.
- 1st settling time of duration 20 ms following the end of the auto-zero correction time.

The various periods of the conversion time are generated via solid-state switches of the ranging amplifier and the integrator; the control signals being generated by the logic circuitry.

VII-1.5.1. 1st Integration.

TS 31 and TS 37 are turned ON and the input voltage to be measured (Vx) is applied to the integrator. When Vx is between 0 V and ± 1 V (1 V, 100 V ranges) TS 36 is also ON and the gain of the ranging amplifier becomes X10.
When Vx is between 1 V and 10 V (10 V, 100 V ranges), TS36 is OFF and the gain of the ranging amplifier is unity.
All other solid-state switches are OFF.
For positive input voltages, the integrator output decreases from 0 V linearly with time.
For negative input voltages, the integrator output increases from 0 V linearly with time.

VII-1.5.2. 2nd Settling time.

At the end of the 1st integration, TS 31 is turned OFF and TS 33 or TS 34 is turned ON, supplying $-V_{REF}$ or $+V_{REF}$ to the ranging amplifier.

However, if the output of the ranging amplifier is applied to the input of the integrator during this dynamic switching state, the resulting transient waveform will be integrated and hence applied to the comparator.
Therefore, at this time TS 37 is also turned OFF, disconnecting the output of the ranging amplifier from the integrator. TS 37 is turned OFF for 100 µs and this is the 2nd settling time.

VII-1.5.3. 2nd Integration.

When TS 37 is turned ON at the end of the 2nd settling, the 2nd integration starts. The polarity of the reference voltage (being applied to the input of the ranging amplifier since the end of the 1st integration) is opposite to the polarity of the input voltage, so that during the 2nd integration the integrator output slopes towards zero volts.
Moreover, if the input voltage is between 0 V and 1 V, TS 32 is turned ON and the reference voltage applied to the ranging amplifier is attenuated by 10.
The end of the 2nd integration occurs when zero volts is reached.
The number of clock pulses counted during the 2nd integration by the Digital section generates the figures in the numerical display.

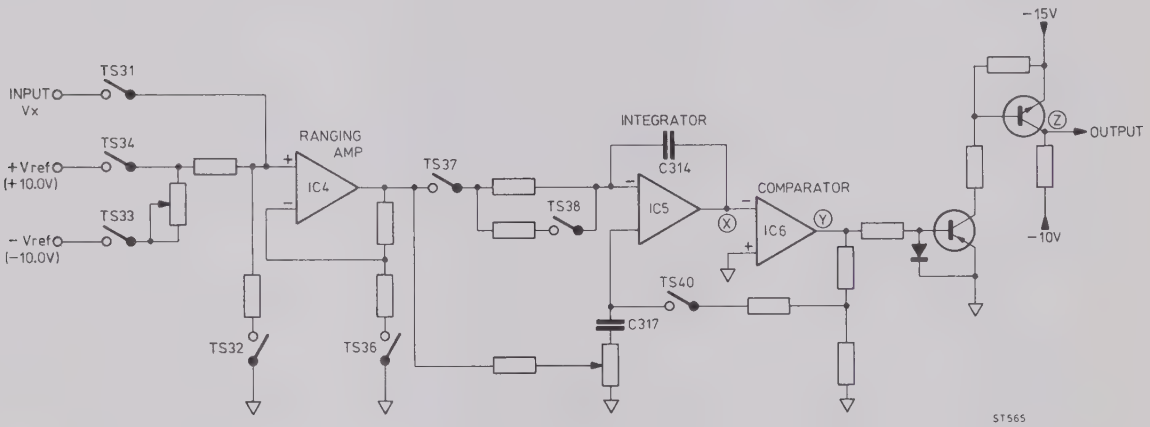


Fig. 13. Analogue-to-digital converter.

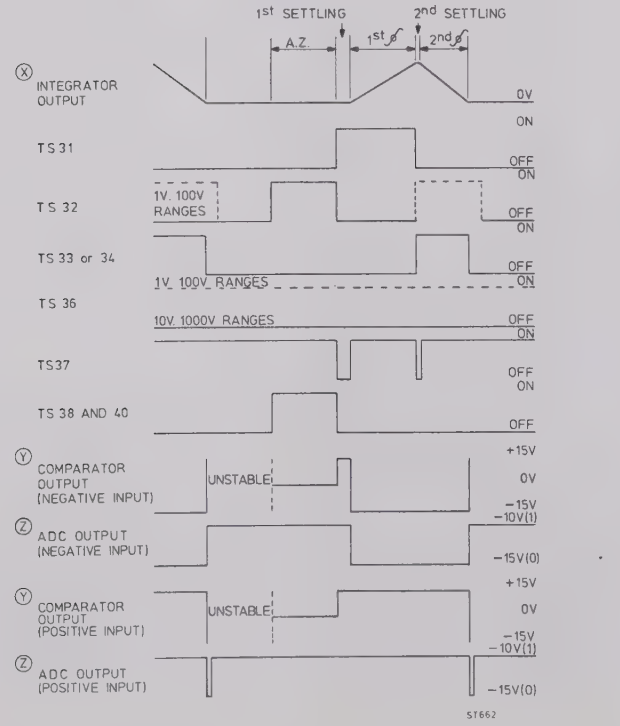


Fig. 14. Sequence diagram ADC.

VII-1.5.4. Auto-zero correction time.

This period starts 200 ms after the start of the 2nd integration (i.e. 100-200 ms after the end of the 2nd integration). TS 38 and TS 40 are turned ON and for 80 ms, capacitor C 317 is charged by the offset voltages of the ranging amplifier, integrating amplifier and comparator. Therefore, at the end of the zero correction time when TS 32, TS 38 and TS 40 are turned OFF, the total offset voltage of the amplifiers in the ADC is stored across capacitor C 317.
This will cancel the offset component of the voltage appearing at the integrator input during the 1st integration. Therefore, only the input voltage signal component effectively charges the integrating capacitor C 314.

VII-1.5.5. 1st Settling time.

At the end of the auto-zero correction time, TS 37 is turned OFF for 20 ms, disconnecting the output of the ranging amplifier from the integrator. This serves the same purpose as the 2nd settling time described in 1.5.2.

General Note: The solid-state switches of the Analogue section are field-effect transistors. They are held OFF by applying - 15 V to the gates.

VII-2. DIGITAL SECTION (Fig. 34).

Reference is made to Fig. 8 - the detailed block diagram. All digital integrated circuits are driven from - 10 V and - 15 V supply voltages.

Logic section
Figs. 15 and 16 are used to explain the operation of the logic section.

VII-2.1. Clock Oscillator.

The clock oscillator produces the time base for the control system and consists of a discrete component astable (TS 50, TS 51, TS 52 and associated components) generating a 100 kHz square wave.

VII-2.2. Counter Driver.

The driver provides the decade counters of the display section with a clock frequency of 100 kHz (IC 12/6). It is inhibited by:

- the clock inhibit pulse (H) at the end of the 1st integration.
- the memory pulse via IC 7/4 at the end of the 2nd integration.

VII-2.3. Auto-zero (AZ).

This consists of a discrete component monostable (TS 60, TS 61 and associated components) which is triggered 200 ms after the start of the 2nd integration. An 80 ms wide pulse (A) is generated.

VII-2.4. 1st Settling.

At the end of the AZ pulse, a 20 ms pulse (G) is produced at IC 12/12 by gating (A), (E) and (F). This is the 1st settling pulse.

VII-2.5. 1st Integration.

The 1st integration commences at the end of the 1st settling pulse when the count extend pulse from IC 15/8 results in a clock pulse at IC 13/5 and IC 13/1 (D) .

- IC 13/9 goes to a logic 0 (E) and hence IC 13/8 to a logic 1. This clears IC 14/1 via GR 124, TS 62 and ensures that IC 14/6 (C) is at a logic 1.
- IC 13/12 remains at a logic 1 (F) .

After a 100 ms, the count extend pulse from IC 15/8 results in the following:

- IC 13/12 goes to a logic 0 (F) , and hence IC 13/13 to a logic 1. This latter transition clocks IC 14/3 and IC 14/6 (C) goes to a logic 0. This ends the 1st integration and the clock inhibit monostable is triggered.

VII-2.6. Clock Inhibit.

At the end of the 1st integration this discrete component monostable (TS 55, TS 56, TS 57 and associated components) is triggered producing a 200 µs pulse (H) which:

- inhibits the counter driver.
- triggers the 2nd settling monostable.

VII-2.7. 2nd Settling time.

At the end of the 1st integration there is a period of 100 µs (I) before the 2nd integration begins. This period is the 2nd settling time and is generated by a discrete component monostable (TS 53, TS 54 and associated components) which is triggered by the clock inhibit monostable at the end of the 1st integration.

VII-2.8. Select Reference Voltage $\pm V_{REF}$.

At the end of the 1st integration, TS 43 or TS 44 is turned ON depending upon the polarity of the input signal.

For positive or a.c. input voltages / positive or a.c. input current, TS 43 is turned OFF and hence TS 33 (ADC) is biassed ON, resulting in $-V_{REF}$ being selected.

For negative input voltages or currents TS 44 is turned OFF and hence TS 34 (ADC) biassed ON, resulting in $+V_{REF}$ being selected.

Note: Except during the 2nd integration, (C) is a logic 1 and consequently TS 43 and TS 44 are both ON and apply - 15 V to the gates of TS 33 and TS 34 (ADC), so biassing them OFF and neither $\pm V_{REF}$ being selected.

VII-2.9. 2nd Integration.

The 2nd integration starts at the end of the 2nd settling period when $\pm V_{REF}$ is applied to the input of the integrator via the ranging amplifier. However, the clock signal to IC 15/7 is inhibited for a further 100 µs by the clock inhibit signal (H) . This period is the zero-dead-band period and partially compensates for the "over" counting at the end of the 2nd integration arising from the hysteresis within the comparator (ADC). The 2nd integration is ended by the zero detect signal clearing IC 14/1 taking IC 14/6 (C) to a logic 1.

VII-2.10. Zero Detect (ZD).

The comparator output (ADC) changes state when the integrator output reaches zero volts. This results in a ZD pulse being produced via TS 62. This brings the 2nd integration to an end.

VII-2.11. Memory.

The memory pulse (J) is generated at the end of the 2nd integration.

- IC 14/6 (C) goes to a logic 1 and hence IC 14/5 to a logic 0. However, the delay introduced by resistor R 246 and capacitor C 341 enables a pulse of a few µs width to be generated at IC 12/8.

The memory pulse inhibits the count via IC 7/4 and transfers the contents of the latches to the multiplexer (IC 15) via IC 7/2. Also polarity and OVER indications for the sample are initiated by the memory pulse.

Note: The memory pulse can be inhibited by the SAMPLE HOLD control applying - 15 V to IC 12/11.

VII-2.12. Polarity Detect.

During the periods of integration IC 14/12 is at a logic 0 / logic 1 for negative / positive input voltages respectively.
IC 14/11 is clocked at the end of the 1st integration when IC 13/12 (F) goes to a logic 0. Therefore at the end of the 2nd integration IC 11/2 is at a logic 0 / logic 1 for negative / positive input voltages respectively.
The memory pulse clocks IC 11/3 at the end of the 2nd integration and IC 11/5 becomes a logic 0 / logic 1 for negative / positive input voltages.
Consequently TS 86 / TS 87 of the display section is turned ON for positive / negative input voltages and (+) / (-) indication is made.

Note: For settings of the function selector other than VDC and mADC IC 11/1 is permanently cleared.
In this case TS 86 and TS 87 are biassed OFF via IC 11/5 and GR 127 respectively. Therefore, no polarity indication is made.

VII-2.13. OVER Detection.

When the memory pulse is generated within 100 ms after the start of the 2nd integration, IC 11/11 is clocked with IC 11/12 (E) at a logic 0. Therefore IC 11/9 is at a logic 0 and TS 85 of the display section is not turned ON via IC 11/9 and OVER indication is not made.
However, if the memory pulse does not occur within the 100 ms period after the start of the 1st integration then IC 11/12 is at a logic 1 when IC 11/11 is clocked. TS 85 is turned ON via IC 11/9 and OVER indication.
If the latter condition occurs when the range is held, then the OVER indicator will be permanently lit.

VII-2.14. Control Functions for FET switches.

This circuitry generates the control functions for the ADC so that the various periods of the conversion time are correctly performed.
When transistors TS 43-TS 49 are turned ON the appropriate FET switches of the ADC are biassed OFF.

Note: The (x10) gain control is at a logic 1 for the 10 V d.c. and 1000 V d.c. ranges only.

VII-2.15. DOWN.

The DOWN signal is a pulse of 9 ms width generated at the end of the 1st integration. The pulse is produced by a discrete component monostable (TS 58, TS 59 and associated components) when triggered via IC 14/6 (G) .
It is used in conjunction with the memory pulse to generate a range-down signal if a more sensitive range setting is possible.

VII-2.16. UP.

The UP signal is generated 100 ms after the end of the 1st integration.
It is used in conjunction with the memory pulse to generate a range-up signal if a less sensitive range setting is required.

VII-2.17. Auto-Range section.

When the contents of the decade counters at the end of the 2nd integration are under 900, range-down is performed.
When the contents are over 10000, range-up is performed.
Otherwise, range-switching is not performed and the range-hold condition is generated.
Refer Fig. 17.

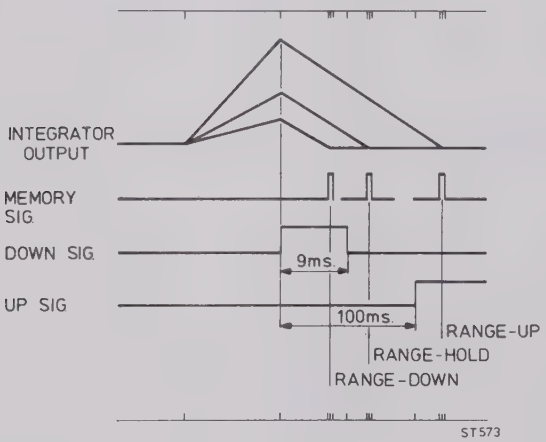


Fig. 17. Up-down timing.

VII-2.18. Up-down counter (Fig. 18).

The UP and DOWN signals from the logic section are gated with the inhibit functions before being applied to the up-down counter.
The J-K flip-flops are clocked by the memory signal and the outputs change state if the appropriate J-K input are at a logic 1. Table 5 explains the arithmetic operation of the circuit.

TABLE 5. Up-DOWN OPERATION.

Control Signals When clocked			JK inputs		
Up	Down	Operation	FF1	FF2	FF3
0	0	Range Hold	0	0	0
0	1	Range Down	1	$\overline{2}^{o}$	$\overline{2}^{o} \cdot \overline{2}'$
1	0	Range Up	1	2^{o}	$\overline{2}^{o} \cdot 2'$
1	1				

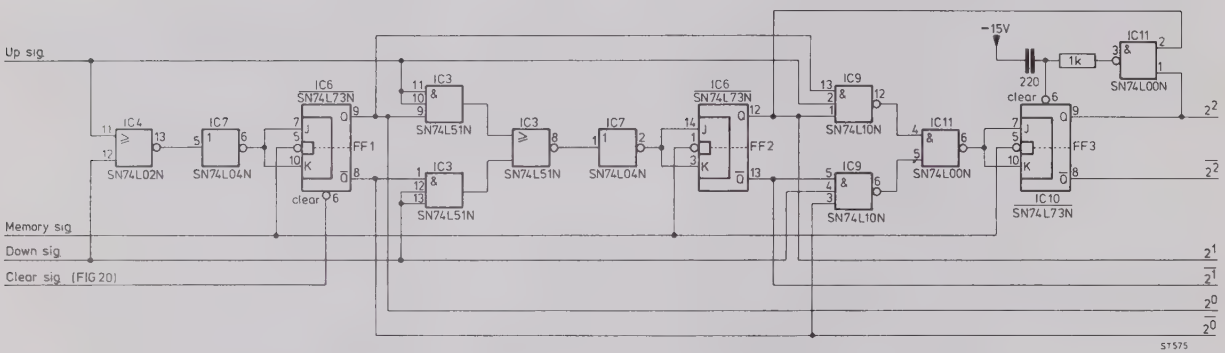


Fig. 18. Up-down Counter.

VII-2.19. Matrix (Fig. 19).

The range-matrix circuit decodes the decimal point position from the binary count signals of the up-down counter. The decoded states are given in Table 6.
The decimal point decodes are also used for the control of the FET switches in the OHM/DCV converter.

TABLE 6. RANGING.

Up -Down Counter output	Range	10^{-4}	10^{-3}	10^{-2}	10^{-1}	10^{o}
2^{o}		1	0	1	0	1
$\overline{2}^{o}$		0	1	0	1	0
$2'$		0	1	1	0	0
$\overline{2}'$		1	0	0	1	1
2^2		0	0	0	1	1
$\overline{2}^2$		1	1	1	0	0

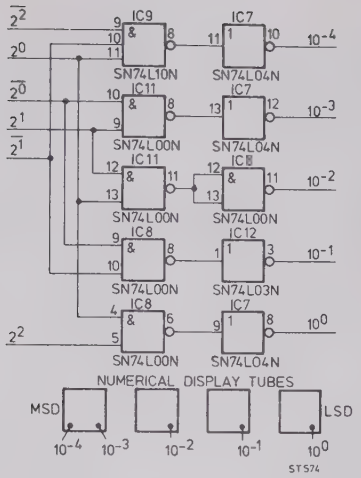


Fig. 19. Range matrix and decimal point positions.

VII-2.20 Inhibit (Fig.20)

This circuitry inhibits the UP signal, DOWN signal or memory signal in order to prevent unnecessary range switching.
The inhibit conditions are:

(a) When a DOWN signal arrives on the 10^{-4} range.

(b) When an UP signal arrives on the 10^{-1} range (with the exception of the kΩ setting)

(c) When an UP signal arrives on the 10^{0} range when set to kΩ.

(d) When the RANGE HOLD terminals are shorted, the memory signal is inhibited via IC 4/8 and the up-down counter is not clocked.

Note: If the 10^{0} range does occur for settings of the function selector other than kΩ, then a clear signal is generated for IC 6/6 via IC 5/3. This action decodes the 10^{-1} range immediately.

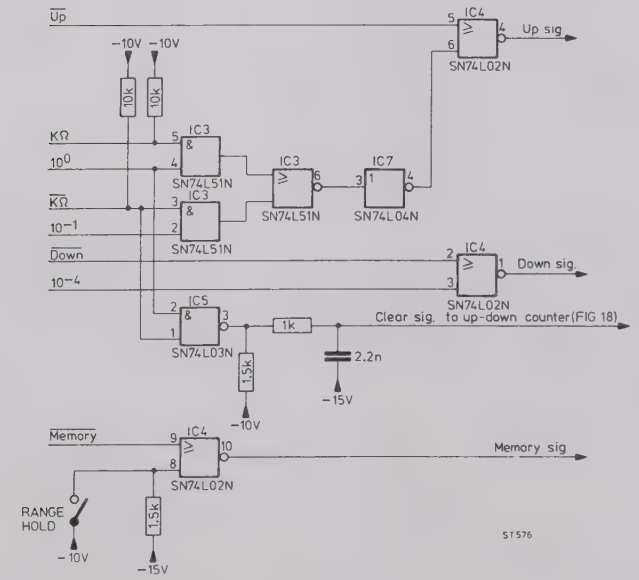


Fig. 20. Inhibit.

VII-2.21. Range control (Fig. 21).

Control functions for the switching of the relays in the input circuitry (Analogue Section) are generated from the matrix outputs.

VII-2.22. Relay drive (Fig. 21).

Table 7 shows the states of the relays of the input circuitry for the various functions and ranges.

TABLE 7. INPUT CIRCUIT RELAY STATES.

Function Selector	Range	DCV Atten.		ACV/DCV					I/V		Gain of input CCT / Shunt resistor
		RE191	RE192	RE193	RE194	RE195	RE196	RE197	RE198	RE199	
VDC	0 - 1 V (10^{-4})	ON	OFF	→	→	→	→	→	→	→	1
	1 - 10 V (10^{-3})	ON	OFF	→	→	→	→	→	→	→	1
	10 - 100 V (10^{-2})	OFF	ON	ON	OFF	→	→	→	→	→	0.01
	100 - 1000 V (10^{-1})	OFF	ON	ON	OFF	→	→	→	→	→	0.01
	1000 - (10^{0})	OFF	→	→	→	→	→	→	→	→	0 (Open Circuit)
VAC	0 - 1 V (10^{-4})	OFF	→	ON	OFF	→	→	→	→	→	1
	1 - 10 V (10^{-3})	OFF	→	→	ON	OFF	→	→	→	→	0.1
	10 - 100 V (10^{-2})	OFF	→	→	ON	ON	OFF	→	→	→	0.01
	100 - 500 V (10^{-1})	OFF	→	→	ON	→	→	OFF	OFF	OFF	0.001
	500 - (10^{0})	OFF	→	→	→	→	→	→	→	→	0 (Open Circuit)
mADC	0 - 1 mA (10^{-4})	OFF	→	→	→	→	→	→	→	→	1 kΩ
	1 - 10 mA (10^{-3})	OFF	→	→	→	→	→	→	→	ON	100 Ω
	10 - 100 mA (10^{-2})	OFF	→	→	→	→	→	→	ON	OFF	10 Ω
	100 - 1000 mA (10^{-1})	OFF	→	→	→	→	→	→	ON	ON	1 Ω
mAAC	0 - 1 mA (10^{-4})	OFF	→	ON	OFF	→	→	→	→	→	1 kΩ
	1 - 10 mA (10^{-3})	OFF	→	ON	OFF	→	→	→	→	ON	100 Ω
	10 - 100 mA (10^{-2})	OFF	→	ON	OFF	→	→	→	ON	OFF	10 Ω
	100 - 1000 mA (10^{-1})	OFF	→	ON	OFF	→	→	→	ON	ON	1 Ω

- ON means the corresponding relay is energised.
- OFF means the corresponding relay is unenergised.

They determine the attenuation at the input circuitry for voltage measurements and select the required shunt resistor for current measurements.

VII-2.23. OHM-FET switch drive (Fig. 21).

The range decodes (10^{-4} ..... 10^{0}) generated by the range-matrix control the FET switches of the OHM/DCV converter. These select the source current for resistance measurements.

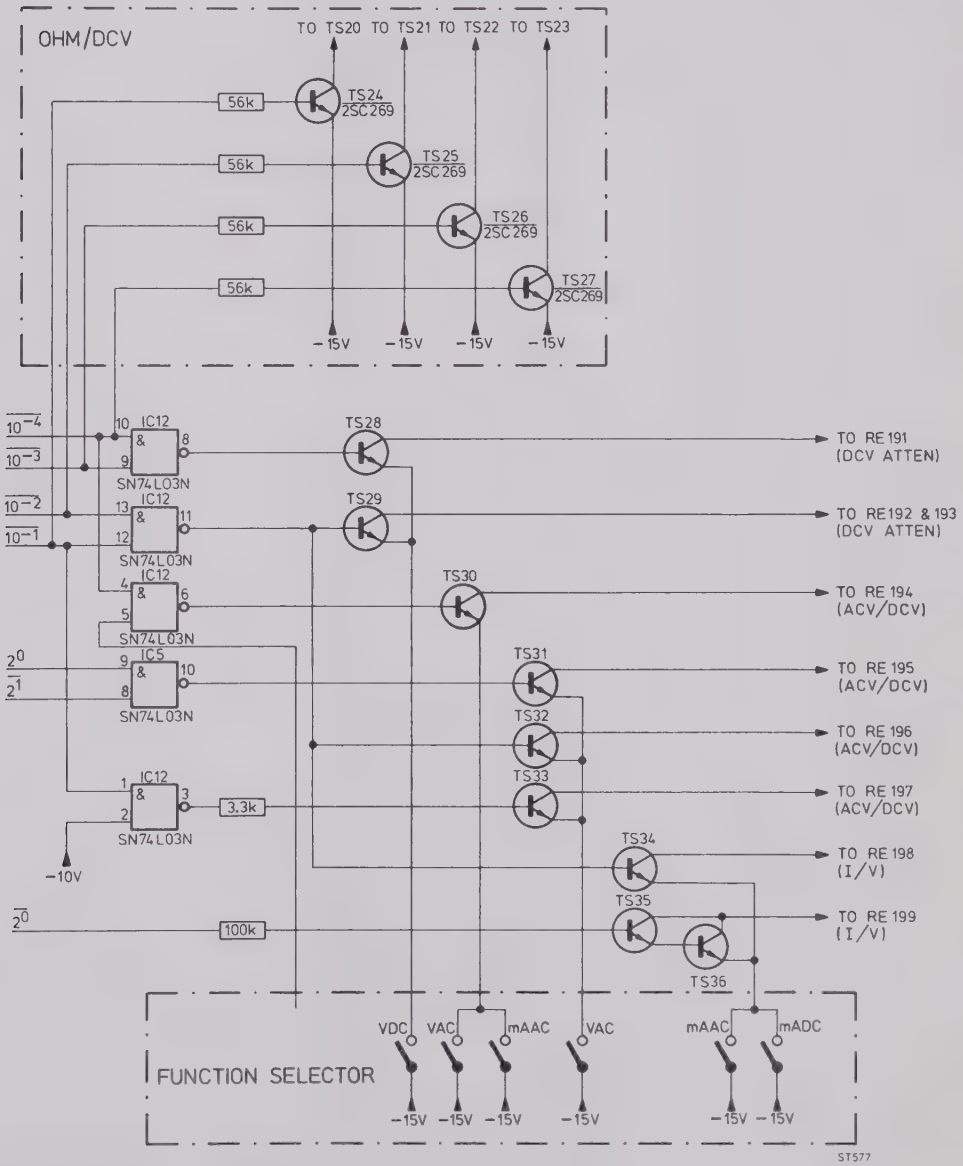


Fig. 21. Range control.

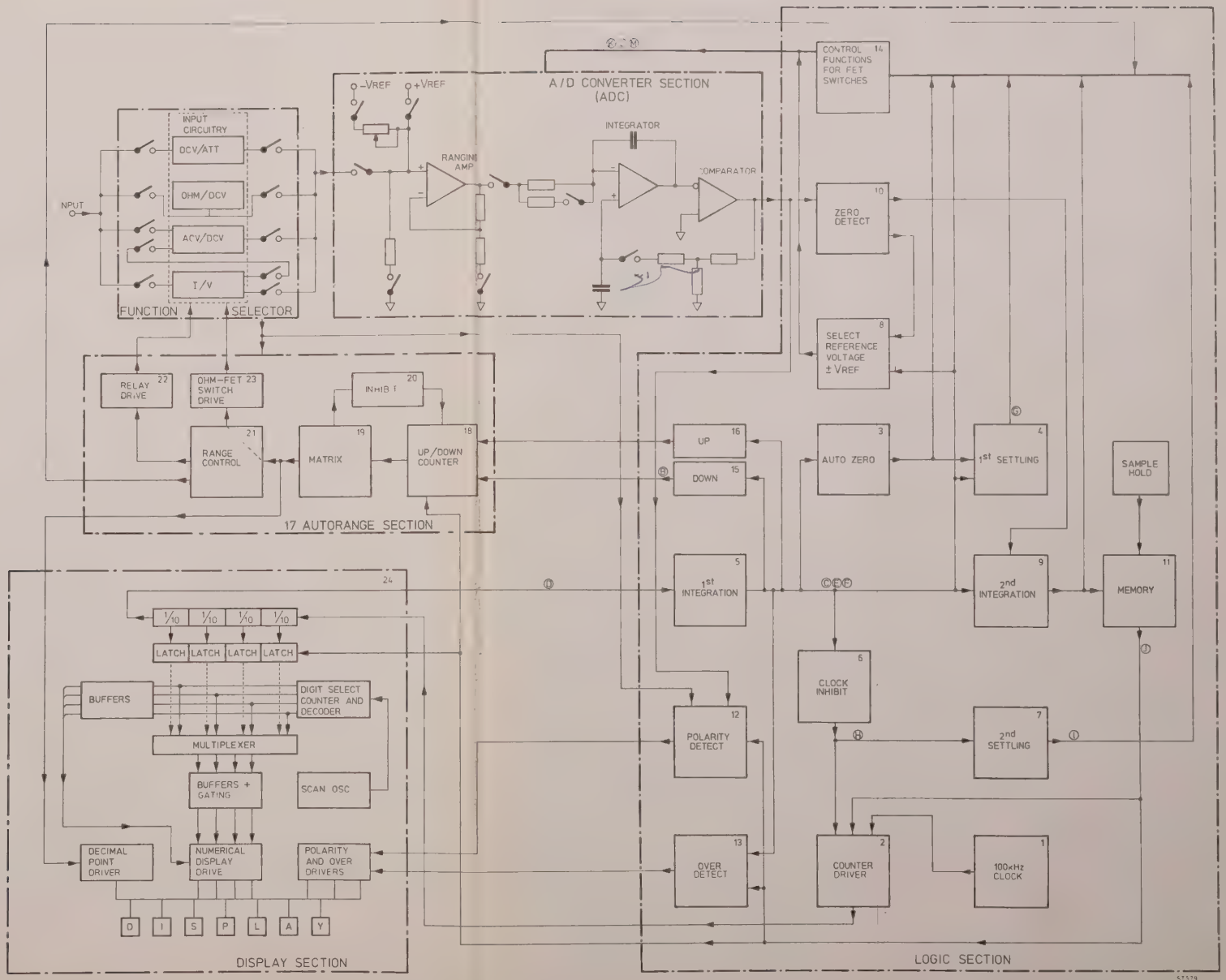


Fig. 8. Detailed block diagram

VII-2.24. Display section (Fig. 22).

The numerical display consists of 4 tubes operating in a dynamic mode where only one of the tubes is ON at any time.
The digit selection signal is generated within IC 15 by an internal 400 Hz oscillator. This scans the digit selection outputs taking them in turn to a logic 1 for 0.6 ms.
The display tube enable pulses are derived from the digit selection outputs.
Synchronisation between the numerical information and the display tube enable pulses is obtained by the digit selection signal enabling the appropriate contents of the multiplexer to be fed to the BCD output terminals.
The BCD outputs are transferred to the display tubes via transistor inverters and a BCD-to-Decimal Decoder/driver.
The BCD counters of IC 15 are clocked at 100 kHz and a count extend pulse is generated after 10^4 clock pulses.
The contents of the counters are transferred to the latches (and therefore to the multiplexer) on the occurrence of the memory signal (waveform M).
The BCD counters and digit select counter/decoder of IC 15 are reset by the clear signal (waveform F).

Display tubes B 373 and B 370 display the most significant (MSD) and least significant (LSD) digits respectively.

VII-3. POWER SUPPLY (Fig. 40).

The voltages required by the instrument are derived via a series regulator and a DC/DC inverter. The DC/DC inverter operates in the self-oscillating mode and the various voltages are derived via the secondary windings of the transformer.

The voltages are:

+ 180 V and + 90 V for the display tubes.

+ 30 V for the series regulator operational amplifier.

+ 15 V for the amplifiers (with - 15 V).

- 10 V for the logic circuitry.

- 15 V (-10 V - logic 1)
(-15 V - logic 0)

The reference voltages $+V_{REF}$ (+ 10.00 V) and $-V_{REF}$ (- 10.00 V) are generated in the ADC section.
Since the stability of the reference voltages has a direct effect upon the accuracy of the measurements, the design of this circuitry ensures stability against environmental changes.

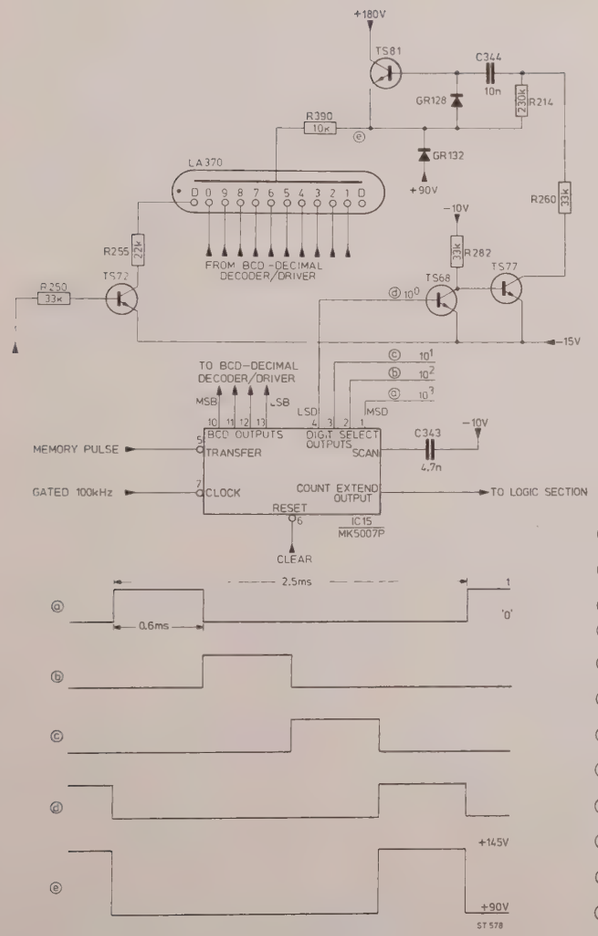


Fig. 22. Plate control and digit selection signal timing.

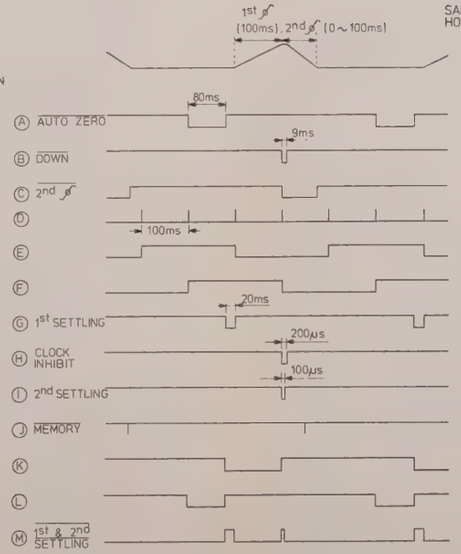


Fig. 15. Timing sequence chart (logic section)

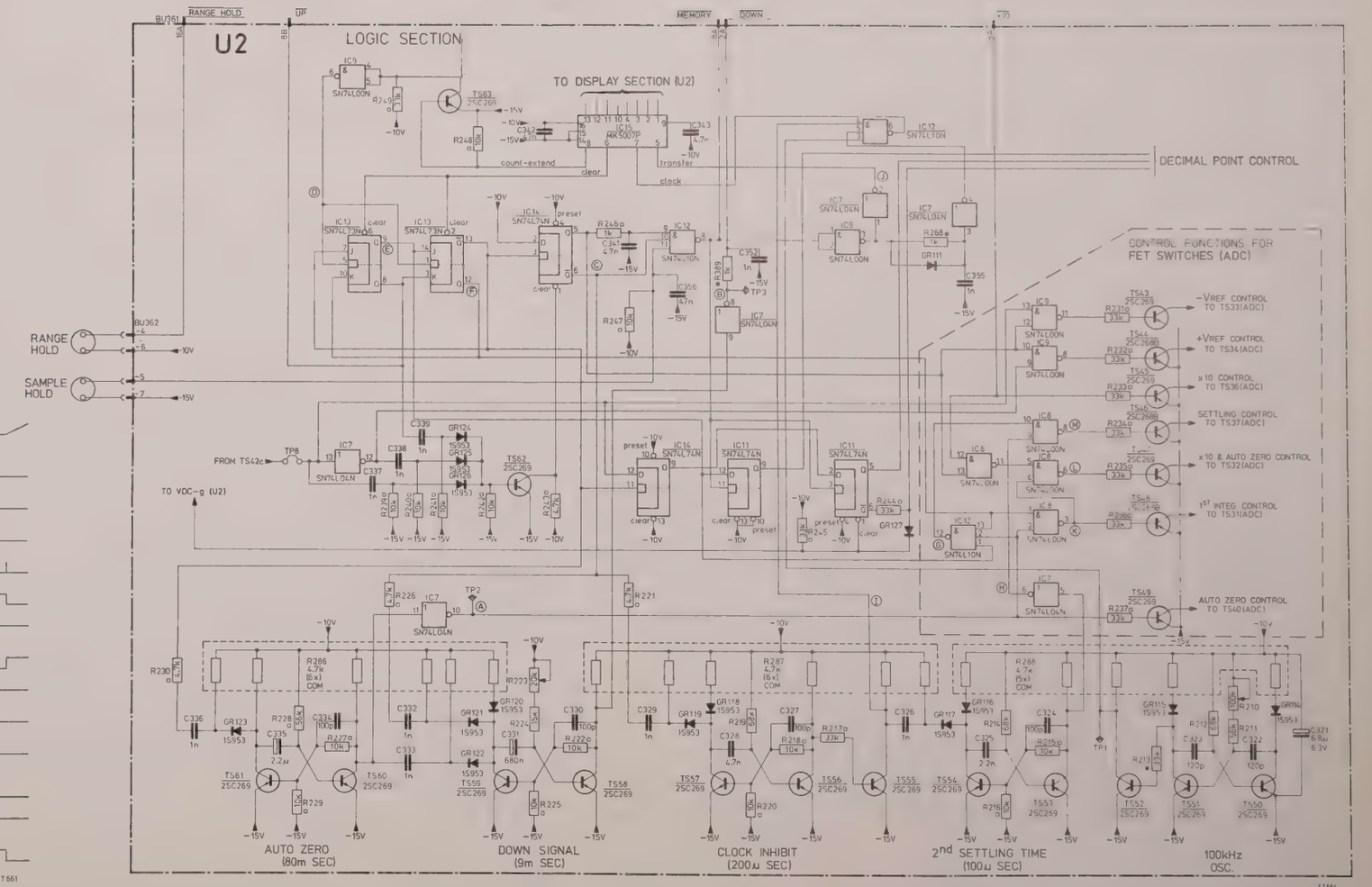


Fig. 16. Circuit diagram of logic section

VIII. Access

VIII-1. CALIBRATION CONTROLS.

By removing the top plate all calibration adjusters are accessible.

VIII-2. BOARD U1 (input circuitry and autorange section).

- Remove the top plate (two screws on the back edge).

  Note: All test points and adjusters on U1 are now accessible.

- Remove the handle and associated parts.
- Remove the left-side section (two screws in rear section; two screws in the base; one screw in blue identification strip).
- Unsolder the wires from the on-board connectors (BU 251-BU 257).
- Remove the three screws from board U1.
- Carefully pull board U1 from connector BU 361 fixed to the lower board (U2).
- Remove board U1.

  Note: The bracket is fixed to board U2.

VIII-3. BOARD U2 (power supply, ADC, logic and display section, function selector).

- Remove the handle and associated parts.
- Remove the top plate (two screws on back edge).
- Remove the blue identification strip on the top front edge (two screws).
- Remove the left-side section (two screws in rear section).
- Unsolder the wires from the on-board connectors (BU 251-BU 257).
- Remove the three screws from board U1.
- Carefully pull board U1 from connector BU 361 fixed to board U2.
- Remove board U1.

  Note: The bracket is fixed to board U2.

The screening card is now exposed.

- Remove the three spacer studs and the screw near the slot in the screening card.
- Remove the screening card with the bracket attached.
- Remove the transparent display cover.
- Disconnect the right-hand section (two screws in rear section; one screw in base) from the main assembly, gently pulling aside as far as the wiring allows.

  Note: All test points and adjusters on U2 are now accessible.

Servicing is made easier by the following actions:

- Remove the function selector surround.
- Remove the base plate (two screws on the back edge).
- Remove the screw passing through the back edge of U2 and pull away the rear section as far as the wiring allows.
- Remove the right-hand section by disconnecting BU 362 and unsoldering from the right-hand section the three wires which are hard-wired from U2.

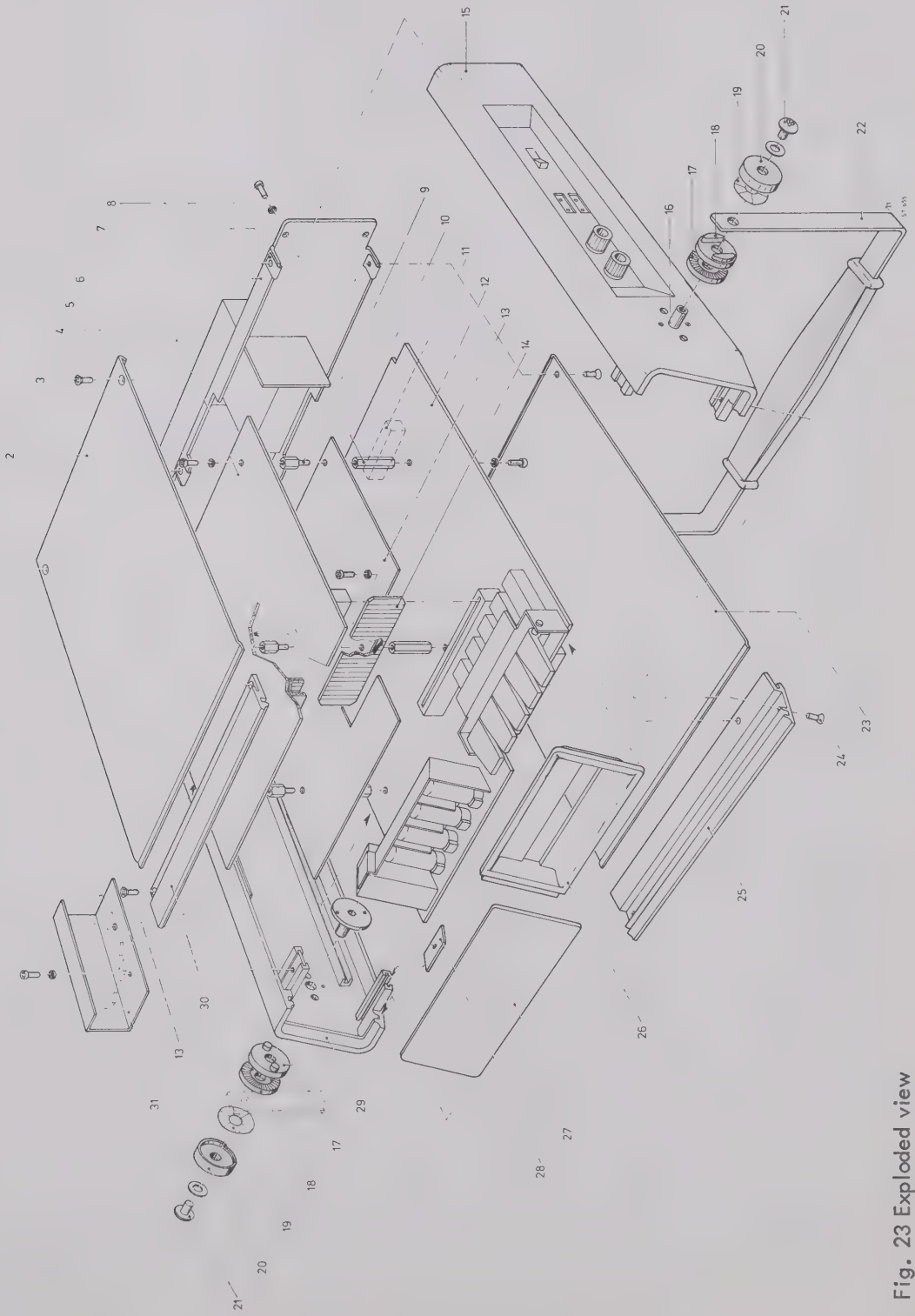


Fig. 23 Exploded view

IX. Checking and calibration

The table below gives a review of the adjustments which can be made to the PM 2424 multimeter.

Control Element	Control Function	Board and Fig.
R 146	Supply voltages	U2
R 210	Clock oscillator frequency 100 kHz	U2
R 223	Pulse width (of Down signal, 9 ms)	U2
R 198	offset corrections	U2 Fig. 24
C 372		U2
	d.c. voltages	
R 171	10 V range ($+V_{REF}$) (negative voltages)	U2, Fig. 24
R 162	10 V range ($-V_{REF}$) (positive voltages)	U2, Fig. 24
R 180	1 V range (negative voltages)	U2, Fig. 24
R 174	1 V range (positive voltages)	U2, Fig. 24
R 132	100 V range	U1, Fig. 24
	a.c. voltages	
R 79	Zero	U1, Fig. 24
R 77	1 V range	U1, Fig. 24
R 65	10 V range (low frequency)	U1, Fig. 24
C 168	10 V range (high frequency)	U1, Fig. 24
R 66	100 V range (low frequency)	U1, Fig. 24
C 169	100 V range (high frequency)	U1, Fig. 24
R 78	500 V range	U1, Fig. 24

Control Element	Control Function	Board and Fig.
	Resistance	
R 120	1 kΩ range	U1, Fig. 24.
R 119	10 kΩ range	U1, Fig. 24
R 102	100 kΩ range	U1, Fig. 24
R 118	1 MΩ range	U1, Fig. 24
R 117	10 MΩ range	U1, Fig. 24

The tolerances stated in this section correspond to the factory data and only apply to a recently adjusted instrument.
When individual components, especially semi-conductors are replaced, the relevant section should be completely readjusted.

Required Test-Equipment.

It is important that the reference equipment used meets the accuracy requirements quoted in the table below.

Test	Required Equipment	Required Performance
Supply voltages	Digital voltmeter Eg. PM 2421	
Clock frequency	Frequency counter Eg. Hewlett-Packard HP5301A	
Pulse width	Oscilloscope Eg. PM 3330	
DC voltage	Standard DC voltage generator	0 - ± 1000 V d.c.
Calibration	Eg. PM 2480	± 0.005% accuracy
AC voltage	Standard AC voltage generator	0 - 500 V r.m.s.
Calibration	Eg. Hewlett-Packard HP745A	± 0.005% accuracy
Resistance measurement	Standard Resistors	900 Ω ± 0.01%
Calibration	Eg. Metal film or Guidline type 9330	9 kΩ ± 0.01% 90 kΩ ± 0.02% 900 kΩ ± 0.1 % 9 MΩ ± 0.2 %

IX-1. CHECKING PROCEDURE.

The test points and adjusters of this section are only accessible when board U1 and the screening card have been removed.
Refer to Access, VIII.2.
Great care must be taken when the instrument is switched ON in this state.

IX-1.1. Supply Voltages.

- Observe the voltage at the - 10 V test point (U 2) and adjust R 146 if necessary to give - 10 V ± 1%.

IX-1.2. Clock Frequency.

- Observe the output at TP1 (U 2) with the frequency counter and adjust R 210 (U 2) if necessary to give 100 kHz ± 0.05%.

IX-1.3. Pulse Width.

- Observe the output at TP3 (U 2) with the oscilloscope and adjust R 223 (U 2) if necessary to give a pulse of 9 ms width.

IX-2. CALIBRATION.

By removing the top plate all calibration adjusters are accessible (Fig. 24), although care is required when adjusting R 162, R 171, R 174 and R 180. Calibration should take place at an ambient temperature of 23°C ± 5°C in a humidity of less than 75%.
Mains or d.c. supply can be used as the power source.
A warm-up period of approximately one hour is required before calibration is made.

Notes:

- Adjustments must be made in the RANGE HOLD mode and not on AUTO RANGE.
- It is important that calibration is carried out in the order as described.
- It may be necessary to temporarily reduce the required input signal in order for the auto-ranging to give the correct range i.e. when -9 V applied at the input, the display may read - 9.00 instead of - 9.000 V. If so, then reduce the input voltage (e.g. to - 8 V) and then increase to - 9 V.

IX-2.1. d.c. voltage (VDC).

- Depress the switch VDC.
- Connect the standard DV voltage generator to the INPUT terminals via the input cable.
- Set the output of the DC voltage generator as given in the table below.
- Apply the RANGE HOLD shorting-plug when the required display format has been obtained.

Order	Range	Input	Adjuster	Display
1	10 V	- 9.000 V	R 171	- 9.000 ± 1 digit
2	10 V	+ 9.000 V	R 162	+ 9.000 ± 1 digit
3	1 V	- 0.9000 V	R 180	- .9000 ± 1 digit
4	1 V	+ 0.9000 V	R 174	+ .9000 ± 1 digit
5	100 V	- 90.00 V	R 132	- 90.00 ± 1 digit

- Disconnect the RANGE HOLD plug and the DC voltage generator.

IX-2.2. a.c. voltage (VAC).

- Depress the switch VAC.
- Connect the standard AC voltage generator to the INPUT terminals via the input cable.
- Set the output of the AC voltage generator as given in the table below.
- Apply the RANGE HOLD shorting-plug when the required display format has been obtained.

Order	Range	Input	Adjuster	Display
1	1 V	0 V	R 79	.0000 ± 1 digit
2	1 V	0.9000 V (400 Hz)	R 77	.9000 ± 1 digit
3	10 V	9.000 V (400 Hz)	R 65	9.000 ± 1 digit
4	10 V	9.000 V (40 kHz)	C 168	9.000 ± 1 digit
5	100 V	90.00 V (400 Hz)	R 66	90.00 ± 1 digit
6	100 V	90.00 V (40 kHz)	C 169	90.00 ± 1 digit
7	600 V	500.0 V (400 Hz)	R 78	500.0 ± 1 digit

- Disconnect the RANGE HOLD plug and AC voltage generator.

IX.2.3. Resistance (kΩ).

- Depress the switch kΩ.
- Connect the standard resistors to the INPUT terminals via the input cable as given in the table below.

Order	Range	Input	Adjuster	Display
1	100 kΩ	90 kΩ	R 102	Standard resistor calibrated value ± 1 digit
2	10000 kΩ	9 MΩ	R 117	Standard resistor calibrated value ± 1 digit
3	1000 kΩ	900 kΩ	R 118	Standard resistor calibrated value ± 1 digit
4	10 kΩ	9 kΩ	R 119	Standard resistor calibrated value ± 1 digit
5	1 kΩ	900 Ω	R 120	Standard resistor calibrated value ± 1 digit

- Disconnect the RANGE HOLD plug and resistor.

IX.2.4. d.c. current (mADC) and a.c. current (mAAC).

d.c. and a.c. current calibration adjusters do not exist.
The measurement accuracy stipulated in the Technical Data (Section II) of this manual is obtained after the completion of the previous adjustments.

X. Maintenance and servicing

The Digital Multimeter PM 2424 does not require any maintenance as the instrument does not contain any components which are subject to excessive wear.
However, this does not apply if the instrument is operated in an environment of excessive heat, moisture, corrosive vapours or excessive dust.

X-1. SERVICE HINTS.

If servicing has to be carried out the following points should be taken into account in order to avoid damaging the instrument.

- Take great care to avoid short-circuits with measuring clips and hooks if the instrument is switched-on, especially near the display section where high voltages are present.
- Use a miniature soldering iron (35 W max.) with a tin cleaner or a vacuum soldering iron.
- Use an acid-free solder.

It is advisable to make an extension connector board for board U1, so that the components of board U2 are accessible with board U1 connected to the instrument.

Notes:

- The use of an extension card requires that the flying-leads to board U1 are extended.
- Components on board U2 are only accessible when the screening card is removed.

X-2. TESTS.

In the following table and oscillograms, voltages and waveforms are given for certain (the more relevant) points of the instrument.

General conditions:

- Nominal mains voltage (220 V, 50 Hz).
- A recently calibrated instrument.
- Voltages measured with respect to 0 V (Black INPUT terminal).
- Oscillograms made with oscilloscope PM 3330.
- Frequency measurements made with Hewlett-Packard HP5301A 10 MHz Counter.
- The data recorded represents the performance of a typical instrument and should serve as a guide only.
- Board U1 connected to board U2 via a suitable extension card.

The table is provided as a general guide to fault finding, but for more specific problems e.g. range switching not performed from 10^{-3} to 10^{-4}, the following TROUBLE SHOOTING section should be consulted.
Moreover, the detailed circuit description of Section VII should enable any fault to be isolated and rectified with ease.

TEST POINT	UNIT	FUNCTION	CONDITION	VALUE	COMMENTS
0 V	U2	A circuit zero		0 V	If an extension card is not used the wires originally connected to BU 256 must be connected to BU 361/10A
BU 250/ 10A		A circuit zero		0 V	
BU 361/ 12A		A circuit zero		0 V	
		POWER SUPPLY			
'c' of TS 24	U2	DC/DC Inverter Frequency		4.45 kHz 0 V to +17.5 V SQ.WAVE	
TP 9 (+ 30 V)	U2	Supply Voltage to Series Regulator (IC1)		+ 29.95 V	
TP 10 (+ 9.0 V nominal)	U2	Output of Series Regulator		+ 8.8 V	
TP 11 (- 15 V)	U2	Supply Voltage to OP. AMPS. and Digital I.C.'s		- 14.9 V	
TP 12 (+ 15 V)	U2	Supply Voltage to OP.AMPS.		+ 14.9 V	
- 10 V	U2	Supply Voltage to Digital I.C.'s		- 9.95 V	
+ 180 V (nominal)	U2	Supply Voltage to Indicators		+ 173.5 V	
+ 90 V (nominal)	U2	Supply Voltage to Indicators		+ 84.5 V	

Note: If an extension card is not used board U1 shoula be completely removed.

		INPUT CIRCUITRY		
BU 253	U1	Output of DCV Attenuator	Switch VDC depressed. Supply respectively to the INPUT terminals:	
			+ 0.5 V d.c.	+ 0.5 V
			+ 5.0 V d.c.	+ 5.0 V
			+ 50 V d.c.	+ 0.5 V
			+ 500 V d.c.	+ 5.0 V

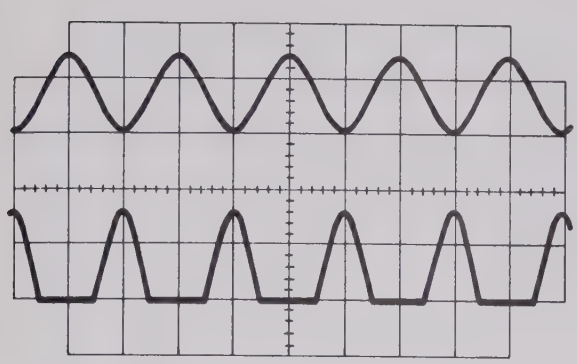
TEST POINT	UNIT	FUNCTION	CONDITION	VALUE	COMMENTS
BU 255	U1	Output of ACV/ DCV Converter	Switch VAC depressed. Supply respectively to the INPUT terminals:		
			0.5 V r.m.s.	+ 0.5 V	
			5.0 V r.m.s.	+ 0.5 V	
			50 V r.m.s.	+ 0.5 V	
			500 V r.m.s. at 1 kHz	+ 0.5 V	
Junction of R 136 and R 137	U1	Output of I/V Converter	Switch mADC depressed. Supply respectively to the INPUT terminals via a 10 kΩ resistor:		(Resistor value: 10.2 kΩ)
			+ 5 V d.c.	440 mV	
			+ 50 V d.c.	480 mV	
			Supply to the INPUT terminals via a 100 Ω resistor:		(Resistor value: 101 Ω)
			+ 5 V d.c.	450 mV	
			Switch mAAC depressed. Supply respectively to the INPUT terminals via a 10 kΩ resistor:		(Resistor value: 10.2 kΩ)
			5 V r.m.s.	1.2 V pk/pk	use oscilloscope
			50 V r.m.s.	1.4 V pk/pk	for measurements
			Supply to the INPUT terminals via a 100 Ω resistor:		(Resistor value: 100 Ω)
			5 V r.m.s.	1.4 V pk/pk	
BU 251	U1	Output of OHM-to DCV Converter	Switch kΩ depressed. Connect between the INPUT terminals respectively:		
			900 Ω	0.9 V	
			9 kΩ	0.9 V	
			90 kΩ	0.9 V	
			900 kΩ	0.9 V	
			5 MΩ	0.5 V	

TEST POINT	UNIT	FUNCTION	CONDITION	VALUE	COMMENTS
'S' of TS 15	U1	ACV/DCV Converter	Switch VAC depressed. Supply to the INPUT terminals 5 V r.m.s. at 1 kHz	Oscillogram Fig. 27.1a	
Cathode of GR 43	U1	ACV/DCV Converter	Switch VAC depressed. Supply to the INPUT terminals 5 V r.m.s. at 1 kHz	Oscillogram Fig. 27.1b	
		ANALOGUE-TO-DIGITAL CONVERTER			If an extension card is not used, BU 256 must be connected to BU 361/10A.
TP 4 (+10.00 V)	U2	$+V_{REF}$		+ 9.994 V	
TP 5 (-10.00 V)	U2	$-V_{REF}$		- 9.994 V	If an extension card is not used, the input signal should be applied to the input of the ADC at R 157 (100 kΩ).
'G' of TS 31	U2	Input of Ranging Amp.	Switch VDC depressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.3	Oscillogram shows the comparison with the integrator output, IC 5/6.
'G' of TS 31	U2	1st integration control	Switch VDC depressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.4	Oscillogram shows the comparison with the integrator output, IC 5/6.
'G' of TS 34	U2	$+V_{REF}$ control	Switch VDC depressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.5	Oscillogram shows the comparison with the integrator output, IC 5/6.
'G' of TS 32	U2	x10 and auto - zero control	Switch VDC depressed. Supply to the INPUT terminals - 0.5 V d.c.	Refer to Fig. 14 (Section VII)	

TEST POINT	UNIT	FUNCTION	CONDITION	VALUE	COMMENTS
'G' of TS 32	U2	x10 and auto - zero control	Switch VDC de-pressed. Supply to the INPUT terminals - 5.0 V d.c.	Refer to Fig. 14 (Section VII)	
'G' of TS 37	U2	Settling times control	Switch VDC de-pressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.6	Oscillogram shows the comparison with the integrator output, IC 5/6.
'G' of TS 38	U2	Auto-zero control	Switch VDC de-pressed. Supply to the INPUT terminals - 5.0 V d.c.	Refer to Fig. 14 (Section VII)	
'G' of TS 40	U2	Auto-zero control	Switch VDC de-pressed. Supply to the INPUT terminals - 5.0 V d.c.	Refer to Fig. 14 (Section VII)	
TP 6	U2	Ranging Amp. Output	Switch VDC de-pressed. Supply to the INPUT terminals + 5.0 V d.c.	Oscillogram Fig. 27.7	Oscillogram shows the comparison with the integrator output, IC 5/6.
TP 7	U2	Comparator Output	Switch VDC de-pressed. Supply to the INPUT terminals + 5.0 V d.c.	Refer to Fig. 14 (Section VII)	
TP 8	U2	Output of ADC	Switch VDC de-pressed. Supply to the INPUT terminals + 5.0 V d.c.	Oscillogram Fig. 27.9	Oscillogram shows the comparison with the integrator output, IC 5/6.
TP 6	U2	Ranging Amp. Output	Switch VDC de-pressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.8	Oscillogram shows the comparison with the integrator output, IC 5/6.
TP 7	U2	Comparator Output	Switch VDC de-pressed. Supply to the INPUT terminals - 5.0 V d.c.	Refer to Fig. 14 (Section VII)	

TEST POINT	UNIT	FUNCTION	CONDITION	VALUE	COMMENTS
TP 8	U2	Output of ADC	Switch VDC depressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.10	Oscillogram shows the comparison with the integrator output, IC 5/6.
		DIGITAL SECTION			If an extension card is not used, BU 256 must be connected to BU 361/10 A.
TP 1	U2	Clock Oscillator 100 kHz		Oscillogram Fig. 27.2	If an extension card is not used, the input signal should be applied to the input of the ADC at R 157 (100 kΩ).
TP 2	U2	$\overline{\text{AUTO-ZERO}}$ Pulse	Switch VDC depressed. Supply to the INPUT terminals - 5.0 V d.c.	Refer to Fig. 15 (Section VII)	
TP 3	U2	$\overline{\text{DOWN}}$ Pulse	Switch VDC depressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.11	Oscillogram shows the comparison with the integrator output, IC 5/6.
BU 361/ 8B	U2	$\overline{\text{UP}}$ Pulse	Switch VDC depressed. Supply to the INPUT terminals - 5.0 V d.c.	Oscillogram Fig. 27.12	Oscillogram shows the comparison with the integrator output, IC 5/6.
BU 361/ 8B	U2	$\overline{\text{UP}}$ Pulse	Switch VDC depressed. Supply to the INPUT terminals approx. - 10.0 V d.c. in order to give an end-of-range value	Oscillogram Fig. 27.13	Oscillogram shows the comparison with the integrator output, IC 5/6.
'c' of TS 63	U2	Count Extend Output	Switch VDC depressed. Supply to the INPUT terminals - 0.5 V d.c.	Oscillogram Fig. 27.14	Oscillogram shows the comparison with the integrator output, IC 5/6.

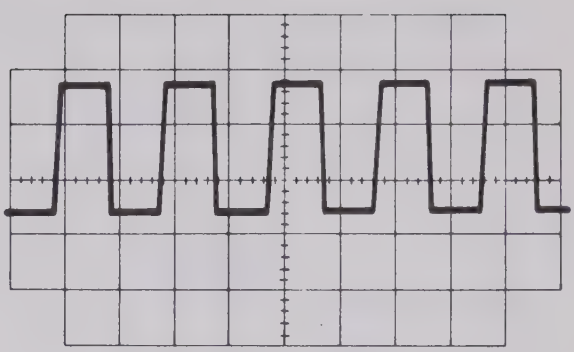
TEST POINT	UNIT	FUNCTION	CONDITION	VALUE	COMMENTS
IC 12/4	U2	Clock Inhibit Pulse	Switch VDC de-pressed. Supply to the INPUT terminals - 0.5 V d.c.	Oscillogram Fig. 27.15	Oscillogram shows the comparison with the integrator output, IC 5/6.
BU 361/ 8A	U2	Memory Pulse	Switch VDC de-pressed. Supply to the INPUT terminals - 0.5 V d.c.	Oscillogram Fig. 27.16	Oscillogram shows the comparison with the integrator output, IC 5/6.
IC 8/8	U2	Settling Pulses	Switch VDC de-pressed. Supply to the INPUT terminals - 0.5 V d.c.	Oscillogram Fig. 27.17	Oscillogram shows the comparison with the integrator output, IC 5/6.



ST 671

Fig. 27.1

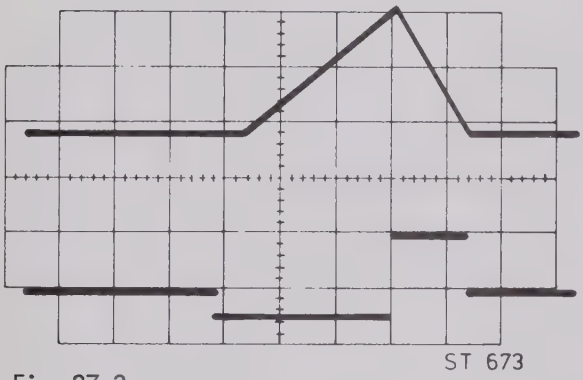
Oscilloscope settings		Comments
Time:	0.5 ms/cm	ACV/DCV
Volts:	a. 1 V/cm b. 1 V/cm	Converter



ST 672

Fig. 27.2

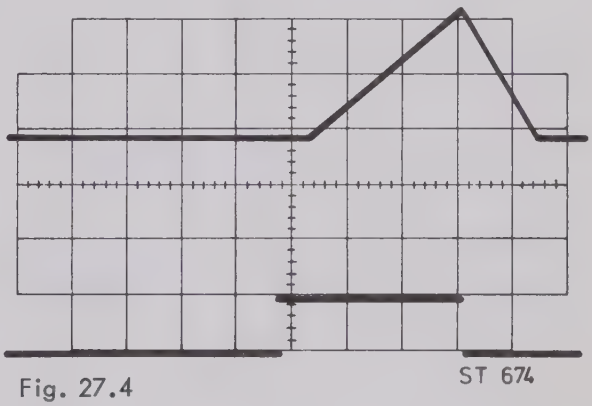
Oscilloscope settings		Comments
Time:	5 µs/cm	100 Khz
Volts:	2 V/cm	Clock oscillator



ST 673

Fig. 27.3

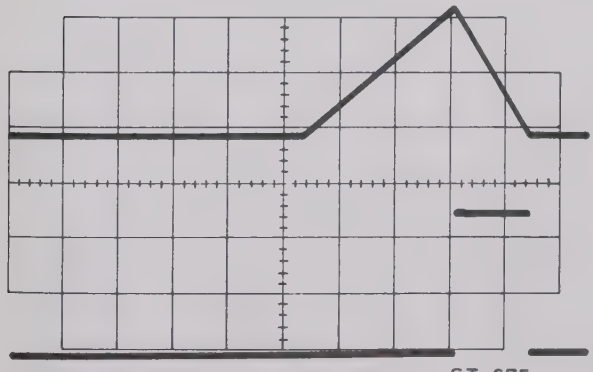
Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 2 V/cm b. 10 V/cm	b. Ranging amp. input



ST 674

Fig. 27.4

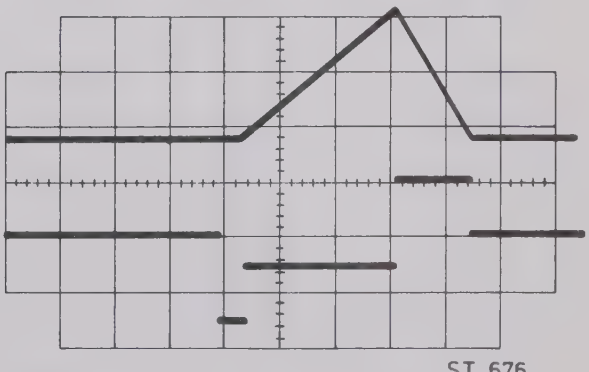
Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 2 V/cm b. 10 V/cm	b. 1st Integration control



ST 675

Fig. 27.5

Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 2 V/cm b. 10 V/cm	b. $+V_{REF}$ select control



ST 676

Fig. 27.6

Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 2 V/cm	b. Settling times control

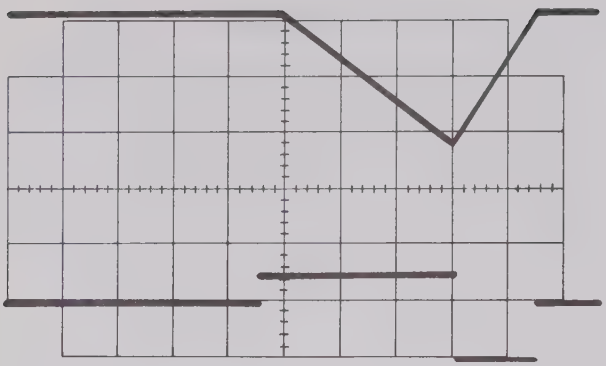


Fig. 27.7 ST 677

Oscilloscope settings	Comments
Time: 100 ms/3 cm	a. Integrator
Volts: a. 2 V/cm b. 10 V/cm	b. Ranging amp. output (+5.0 V input)

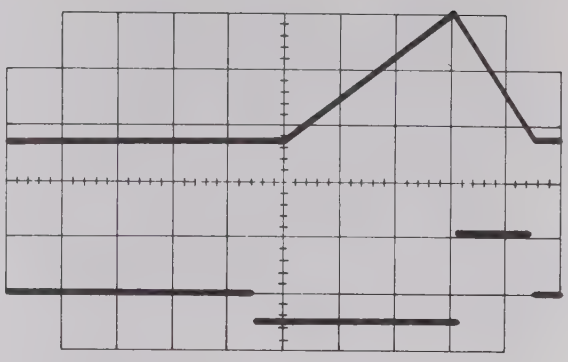


Fig. 27.8 ST 678

Oscilloscope settings	Comments
Time: 100 ms/3 cm	a. Integrator
Volts: a. 2 V/cm b. 10 V/cm	b. Ranging amp. output (-5.0 V input)

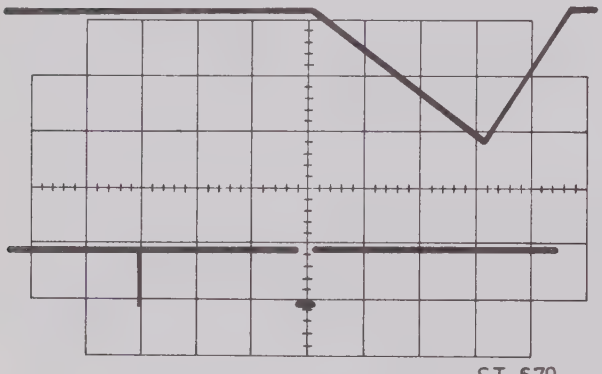


Fig. 27.9 ST 679

Oscilloscope settings	Comments
Time: 100 ms/3 cm	a. Integrator
Volts: a. 2 V/cm b. 5 V/cm	b. Output of ADC (+5.0 V input)

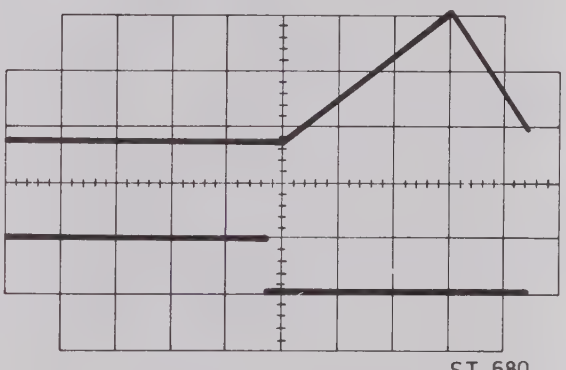


Fig. 27.10 ST 680

Oscilloscope settings	Comments
Time: 100 ms/3 cm	a. Integrator
Volts: a. 2 V/cm b. 5 V/cm	b. Output of ADC (-5.0 V input)

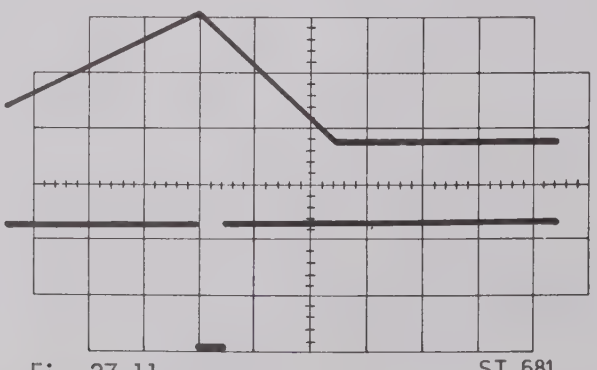


Fig. 27.11 ST 681

Oscilloscope settings	Comments
Time: 20 ms/cm	a. Integrator
Volts: a. 2 V/cm b. 2 V/cm	b. Down pulse

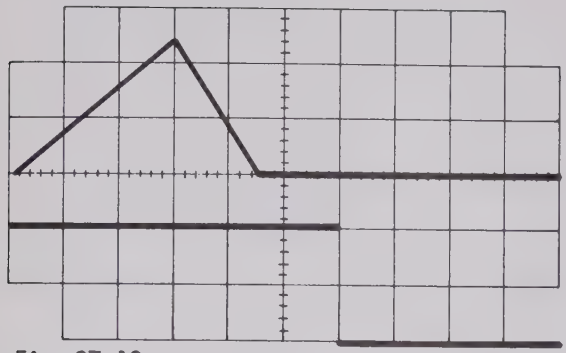


Fig. 27.12 ST 682

Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 2 V/cm	b. Up signal (-5.0 V input)
	b. 2 V/cm	

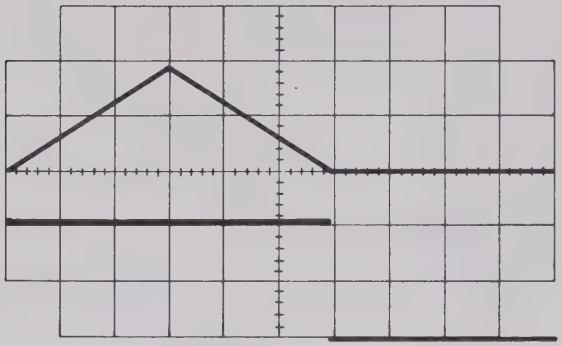


Fig. 27.13 ST 683

Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 5 V/cm	b. Up signal (at end-of-range)
	b. 2 V/cm	

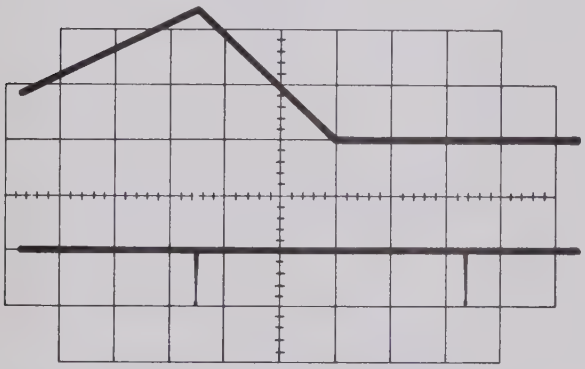


Fig. 27.14 ST 684

Oscilloscope settings		Comments
Time:	20 ms/cm	a. Integrator
Volts:	a. 2 V/cm	b. "Count extend" output
	b. 5 V/cm	

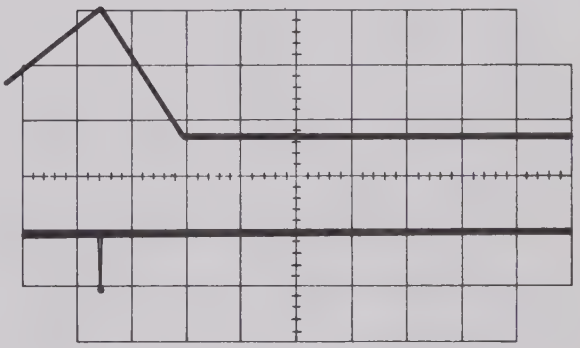


Fig. 27.15 ST 685

Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 2 V/cm	b. Clock inhibit pulse
	b. 5 V/cm	

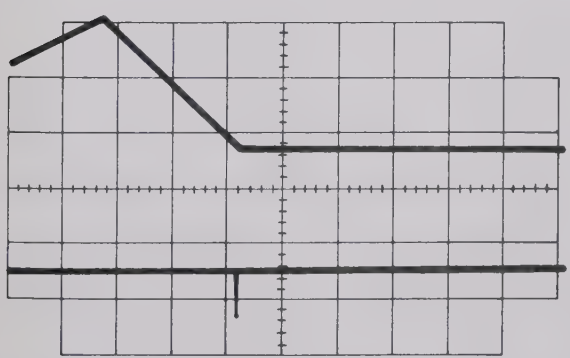


Fig. 27.16 ST 686

Oscilloscope settings		Comments
Time:	20 ms/cm	a. Integrator
Volts:	a. 2 V/cm	b. Memory (transfer) pulse
	b. 5 V/cm	

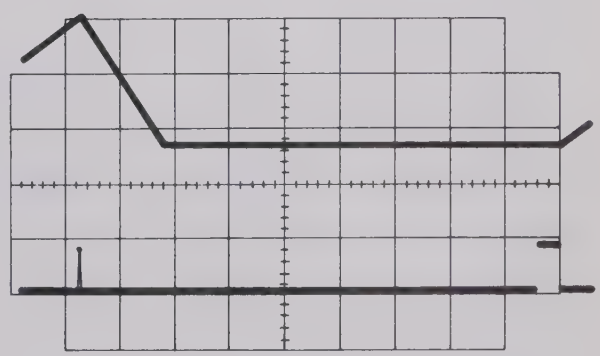
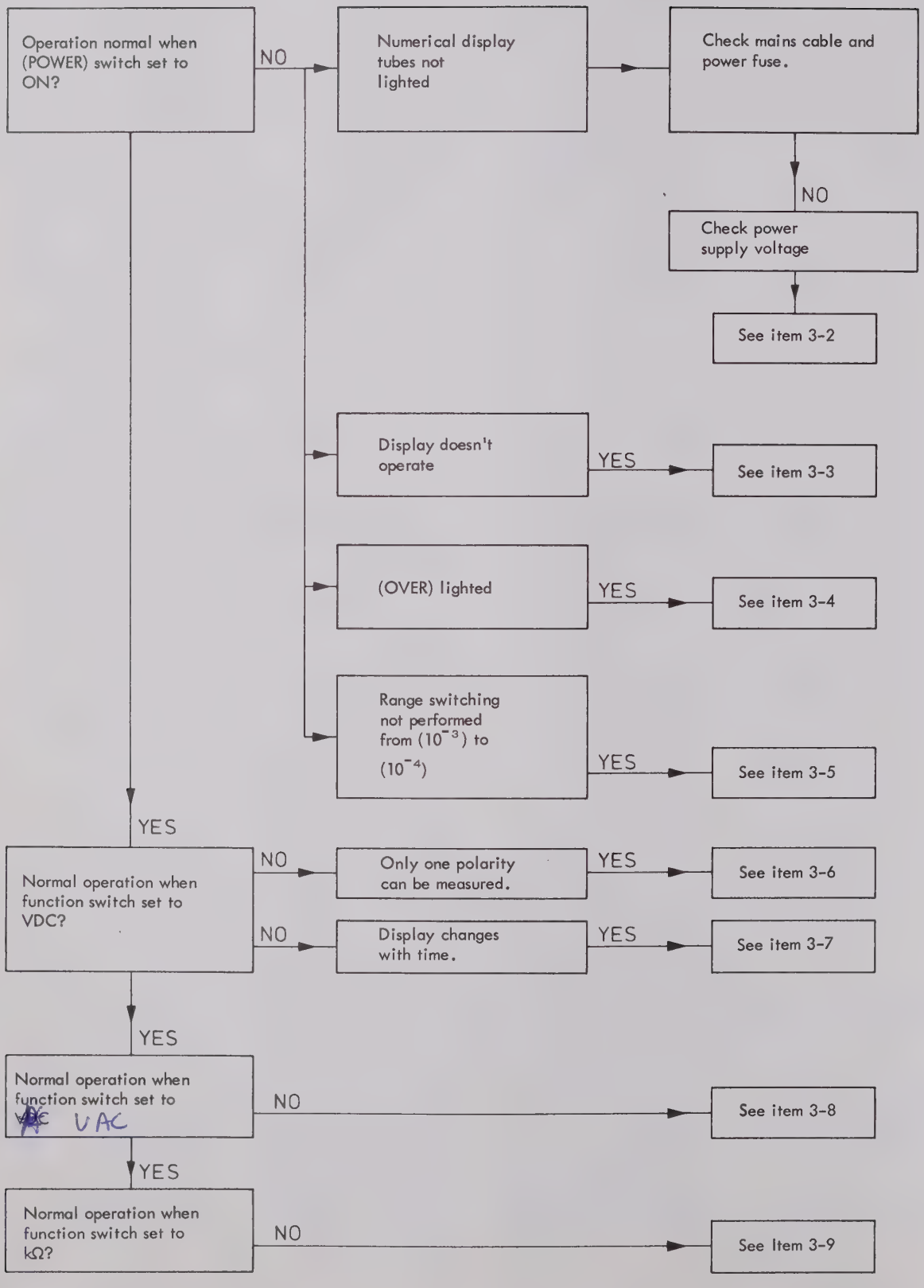


Fig. 27.17 ST 687

Oscilloscope settings		Comments
Time:	100 ms/3 cm	a. Integrator
Volts:	a. 2 V/cm	b. Settling pulse
	b. 5 V/cm	

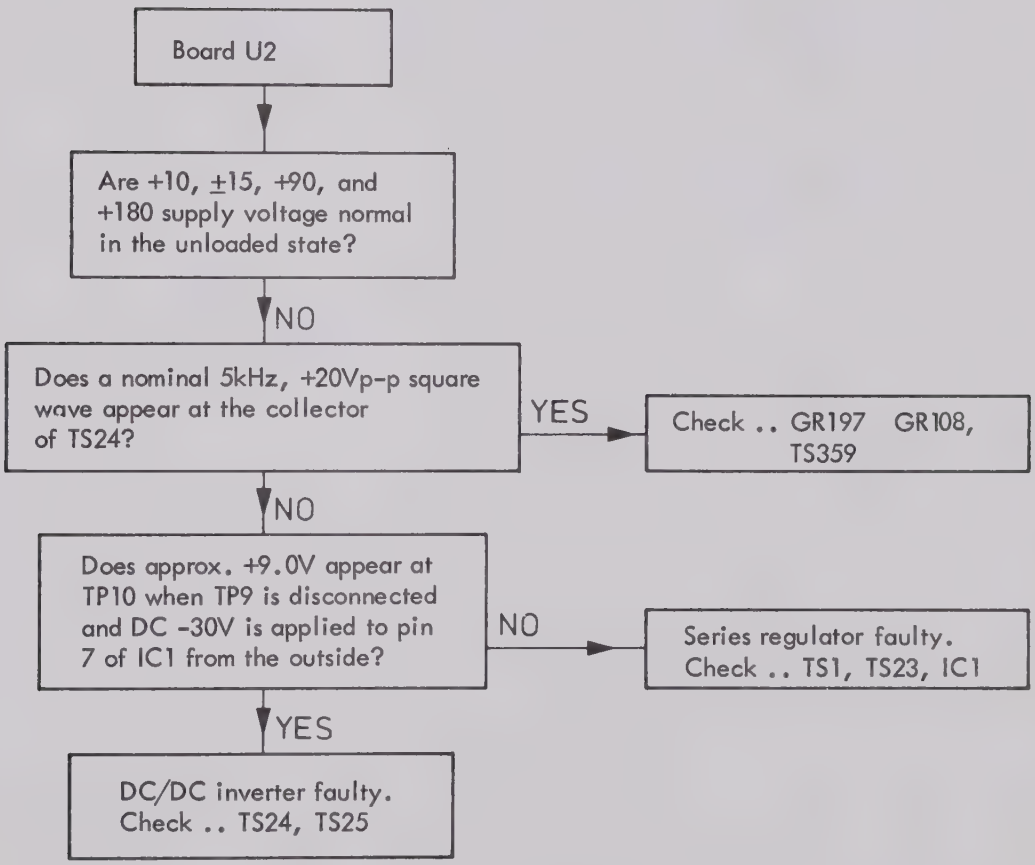
X-3 TROUBLESHOOTING

X-3.1.

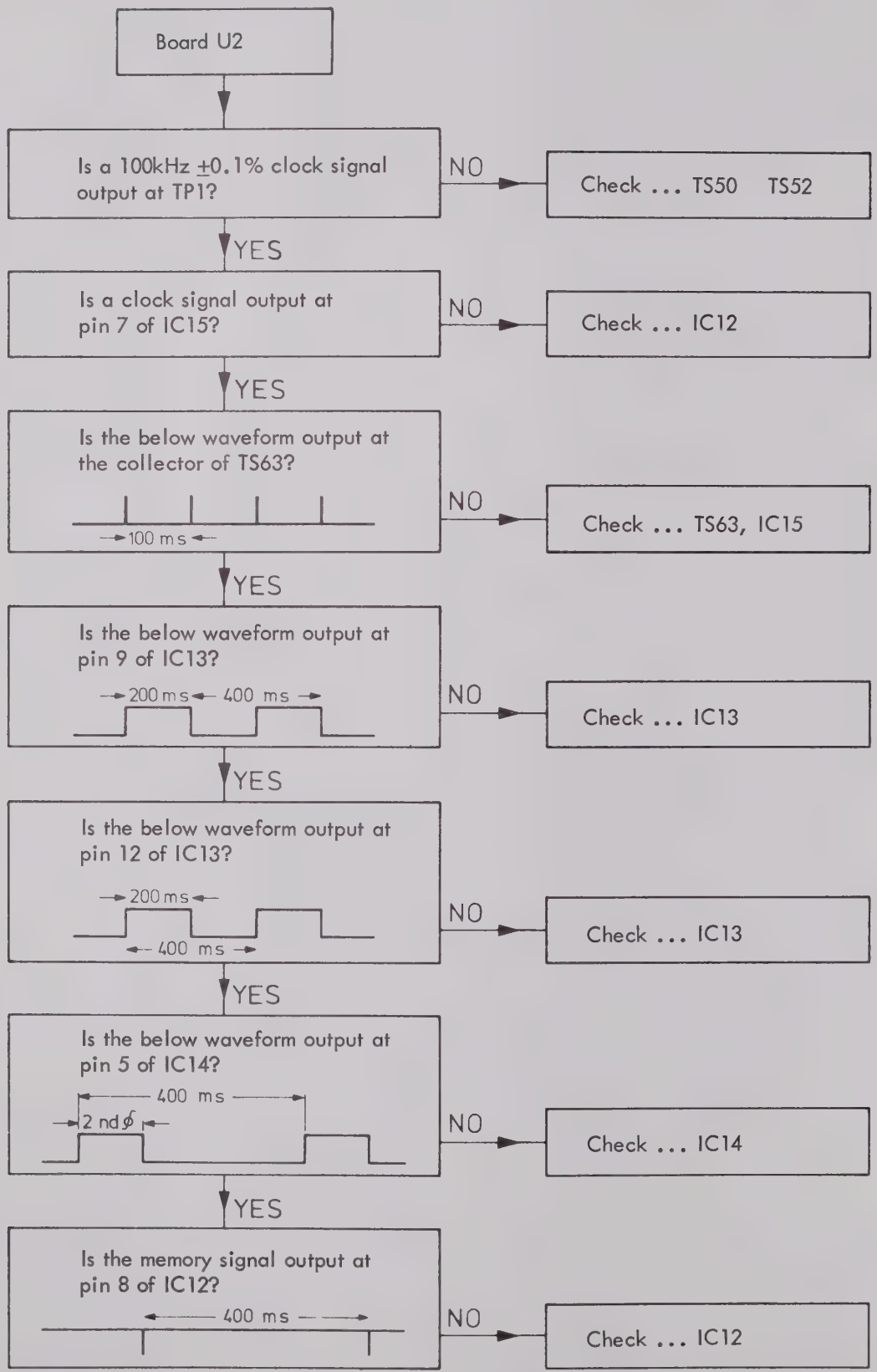


ST 688

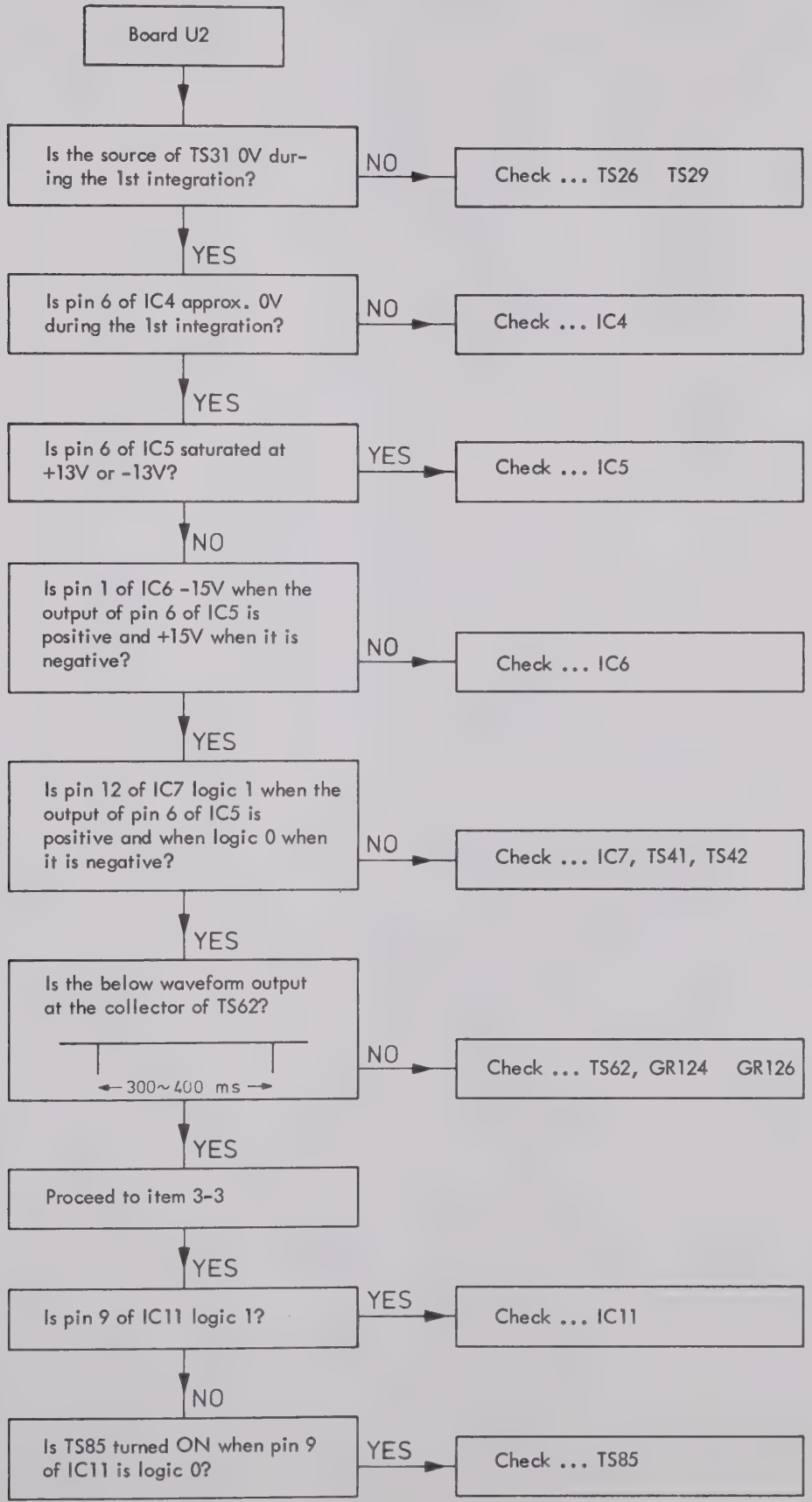
X-3.2 Power supply voltage check


Board U2
Are +10, ±15, +90, and +180 supply voltage normal in the unloaded state?
NO
Does a nominal 5kHz, +20Vp-p square wave appear at the collector of TS24?
YES
Check .. GR197 GR108, TS359
NO
Does approx. +9.0V appear at TP10 when TP9 is disconnected and DC -30V is applied to pin 7 of IC1 from the outside?
NO
Series regulator faulty. Check .. TS1, TS23, IC1
YES
DC/DC inverter faulty. Check .. TS24, TS25

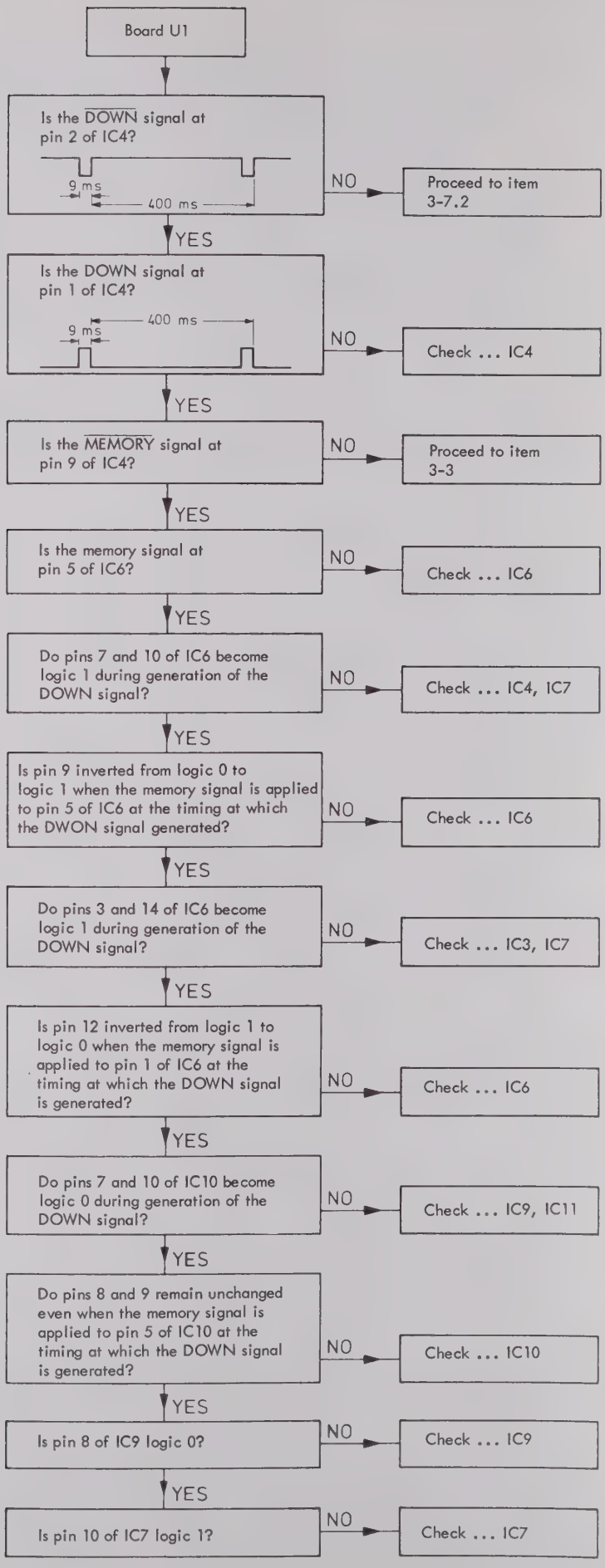
X-3.3 A certain value is displayed continually



X-3.4. (OVER) Lighted even when all the function switches are OFF

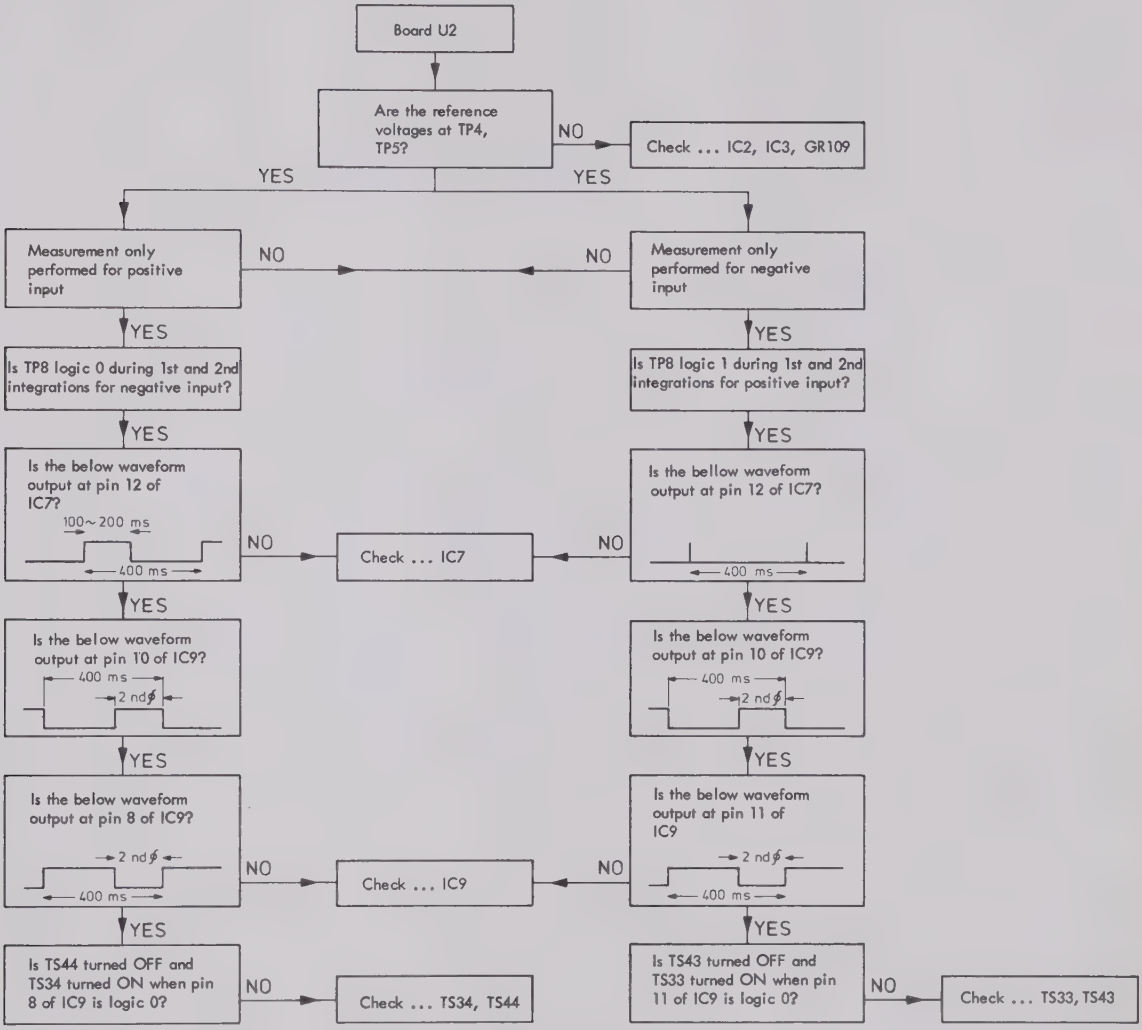

Board U2
Is the source of TS31 0V during the 1st integration?
NO
Check ... TS26 TS29
YES
Is pin 6 of IC4 approx. 0V during the 1st integration?
NO
Check ... IC4
YES
Is pin 6 of IC5 saturated at +13V or -13V?
YES
Check ... IC5
NO
Is pin 1 of IC6 -15V when the output of pin 6 of IC5 is positive and +15V when it is negative?
NO
Check ... IC6
YES
Is pin 12 of IC7 logic 1 when the output of pin 6 of IC5 is positive and when logic 0 when it is negative?
NO
Check ... IC7, TS41, TS42
YES
Is the below waveform output at the collector of TS62?
300~400 ms
NO
Check ... TS62, GR124 GR126
YES
Proceed to item 3-3
YES
Is pin 9 of IC11 logic 1?
YES
Check ... IC11
NO
Is TS85 turned ON when pin 9 of IC11 is logic 0?
YES
Check ... TS85

X-3.5. Range switching is not performed from (10^{-3}) to (10^{-4})


Board U1
Is the DOWN signal at pin 2 of IC4?
9 ms
400 ms
NO
Proceed to item 3-7.2
YES
Is the DOWN signal at pin 1 of IC4?
400 ms
9 ms
NO
Check ... IC4
YES
Is the MEMORY signal at pin 9 of IC4?
NO
Proceed to item 3-3
YES
Is the memory signal at pin 5 of IC6?
NO
Check ... IC6
YES
Do pins 7 and 10 of IC6 become logic 1 during generation of the DOWN signal?
NO
Check ... IC4, IC7
YES
Is pin 9 inverted from logic 0 to logic 1 when the memory signal is applied to pin 5 of IC6 at the timing at which the DWON signal generated?
NO
Check ... IC6
YES
Do pins 3 and 14 of IC6 become logic 1 during generation of the DOWN signal?
NO
Check ... IC3, IC7
YES
Is pin 12 inverted from logic 1 to logic 0 when the memory signal is applied to pin 1 of IC6 at the timing at which the DOWN signal is generated?
NO
Check ... IC6
YES
Do pins 7 and 10 of IC10 become logic 0 during generation of the DOWN signal?
NO
Check ... IC9, IC11
YES
Do pins 8 and 9 remain unchanged even when the memory signal is applied to pin 5 of IC10 at the timing at which the DOWN signal is generated?
NO
Check ... IC10
YES
Is pin 8 of IC9 logic 0?
NO
Check ... IC9
YES
Is pin 10 of IC7 logic 1?
NO
Check ... IC7

ST 691

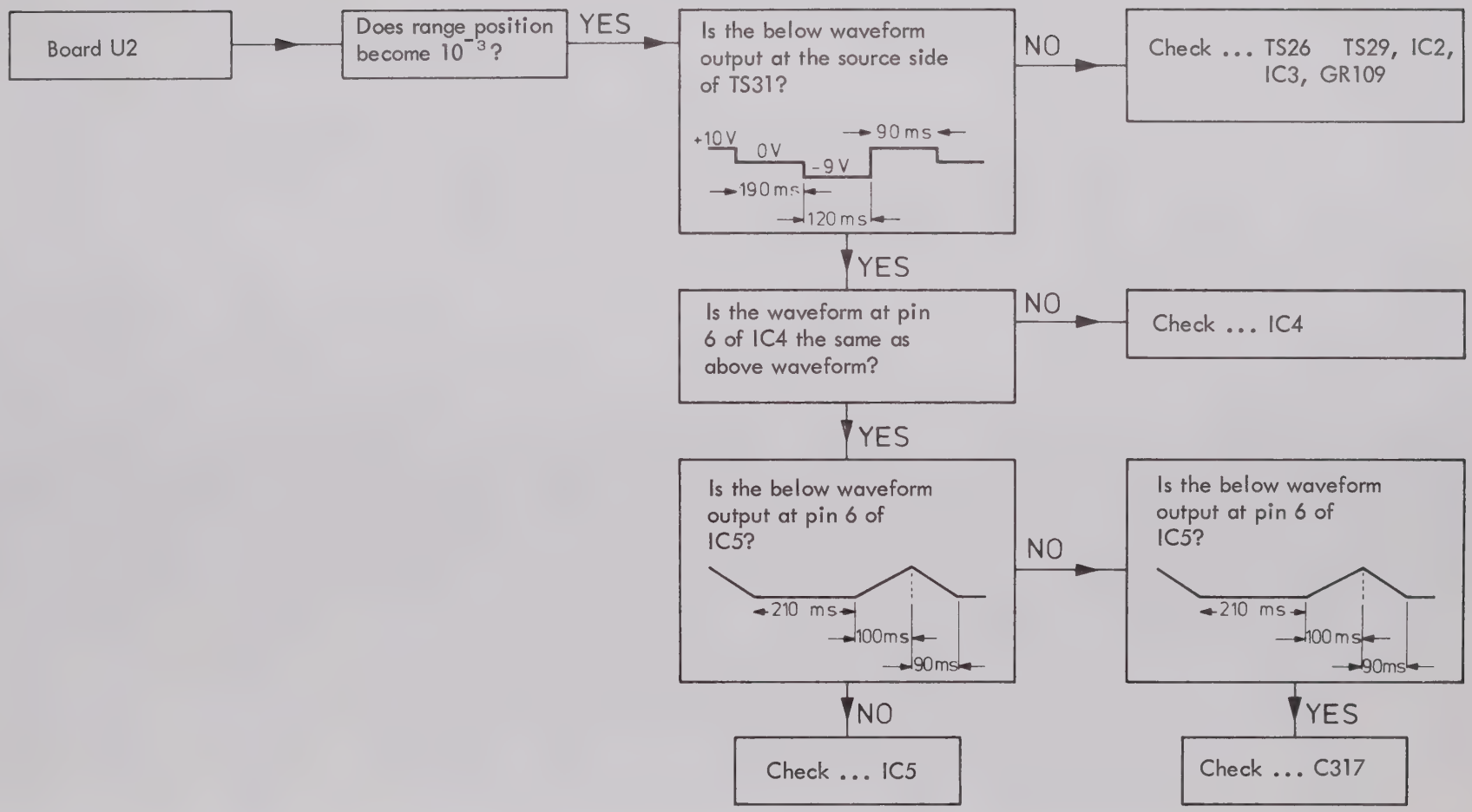
X-3.6. Only one polarity can be measured



X-3.7 Display changes with time.

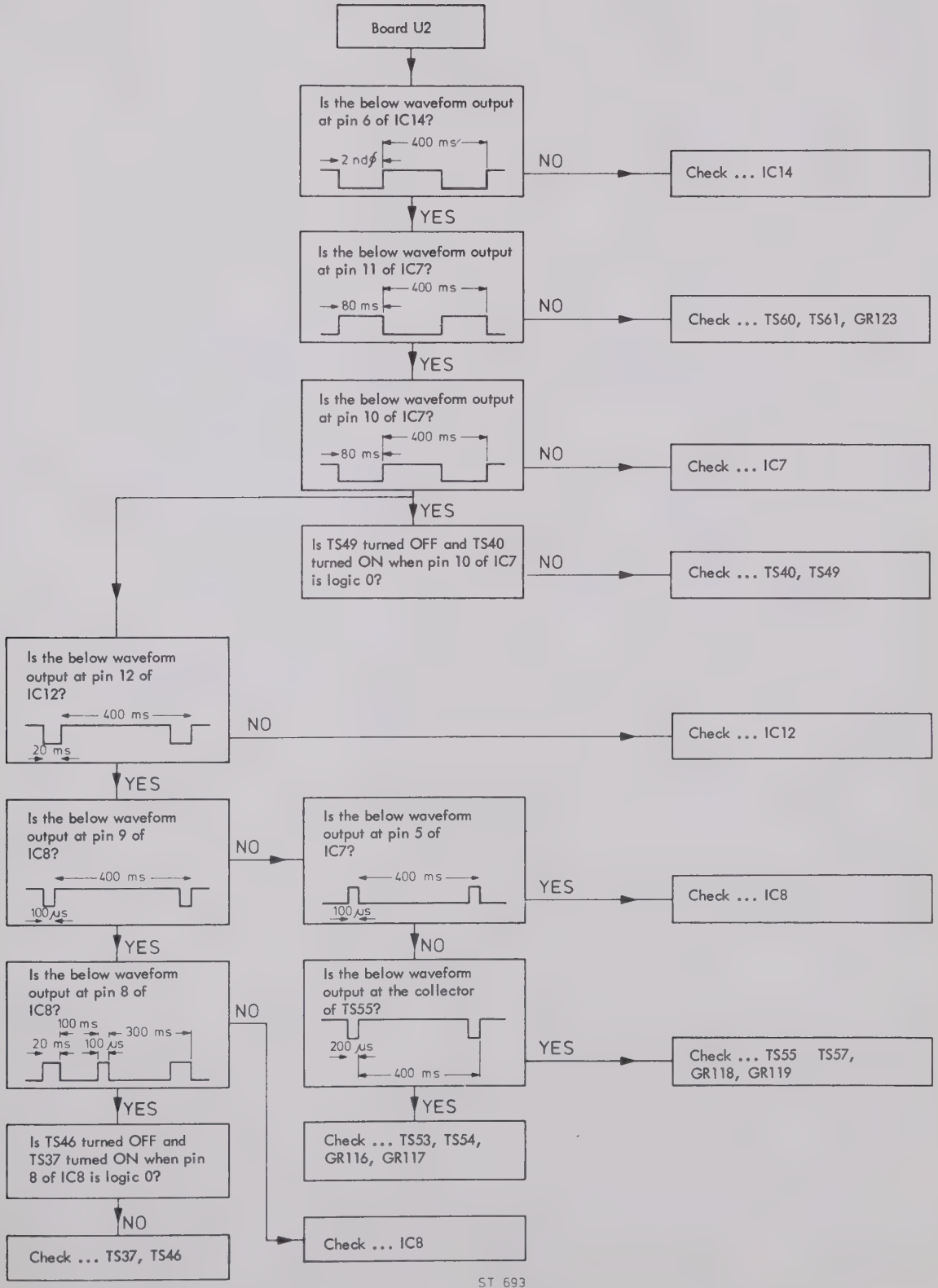
X-3.7.1 Analogue Section.

-9V applied to input terminals

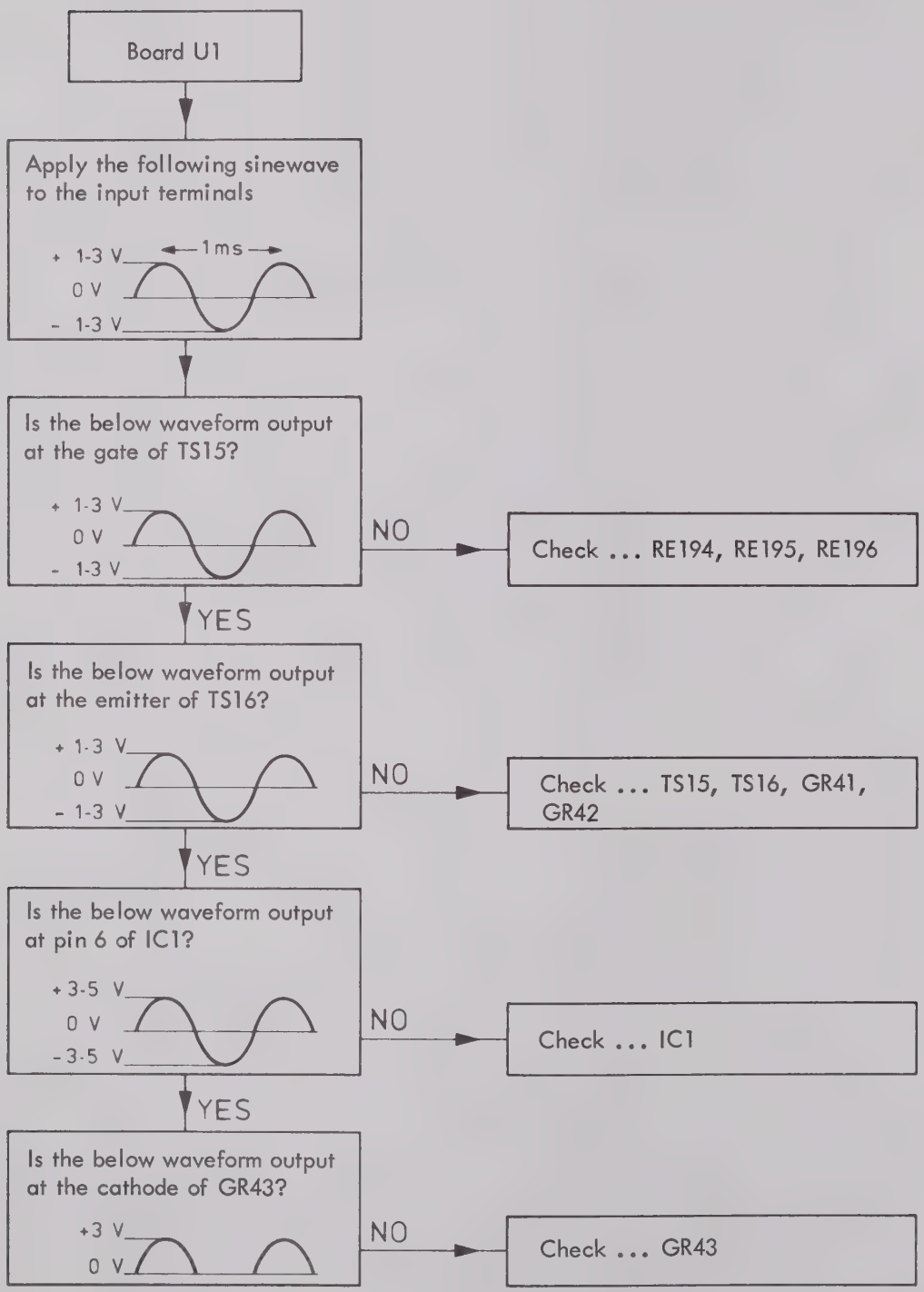


ST 692

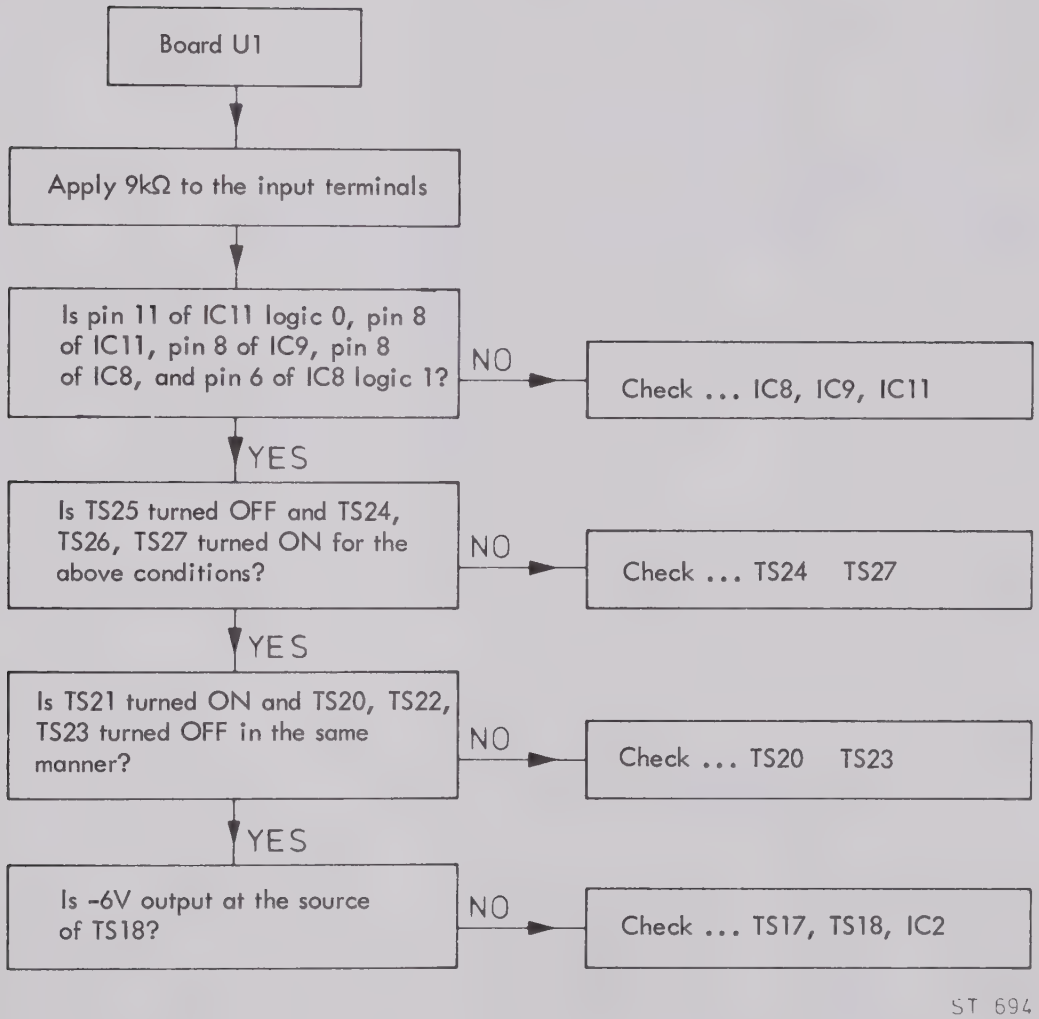
X-3.7.2. Logic Section



X-3.8 Correct measurement is not performed when measuring AC voltage



X-3.9 Correct measurement is not performed in resistance measurement.


Board U1
Apply 9kΩ to the input terminals
Is pin 11 of IC11 logic 0, pin 8 of IC11, pin 8 of IC9, pin 8 of IC8, and pin 6 of IC8 logic 1?
NO
Check ... IC8, IC9, IC11
YES
Is TS25 turned OFF and TS24, TS26, TS27 turned ON for the above conditions?
NO
Check ... TS24 TS27
YES
Is TS21 turned ON and TS20, TS22, TS23 turned OFF in the same manner?
NO
Check ... TS20 TS23
YES
Is -6V output at the source of TS18?
NO
Check ... TS17, TS18, IC2

ST 694

X-4. SEMICONDUCTORS.

Integrated circuit IC 15 is an LSI MOS 4-digit counter/display decoder (type MK 5007 P).
The pin connections of this device are given in Fig. 28.
The truth tables below explain the behaviour of the device.

INPUT TRUTH TABLE	
INPUT	LOGIC CONDITION TO ACTIVATE
$\overline{\text{COUNT}}$	NEGATIVE EDGE
$\overline{\text{RESET}}$	0
$\overline{\text{TRANSFER}}$	0
SCAN	1 (POSITIVE EDGE)

OUTPUT TRUTH TABLE		
OUTPUT	TRUE LOGIC STATE	TIME OF OCCURRENCE
DIGIT SELECT	1	One-of-four, following SCAN input rising edge; all off when SCAN input is 0.
COUNT EXTEND	1	Occurs each time the counter state attains 9999 count. Remains true only until the next COUNT INPUT or RESET occurs (when the counter returns to 0000).
BCD		BCD count sequence clocked by COUNT INPUT.

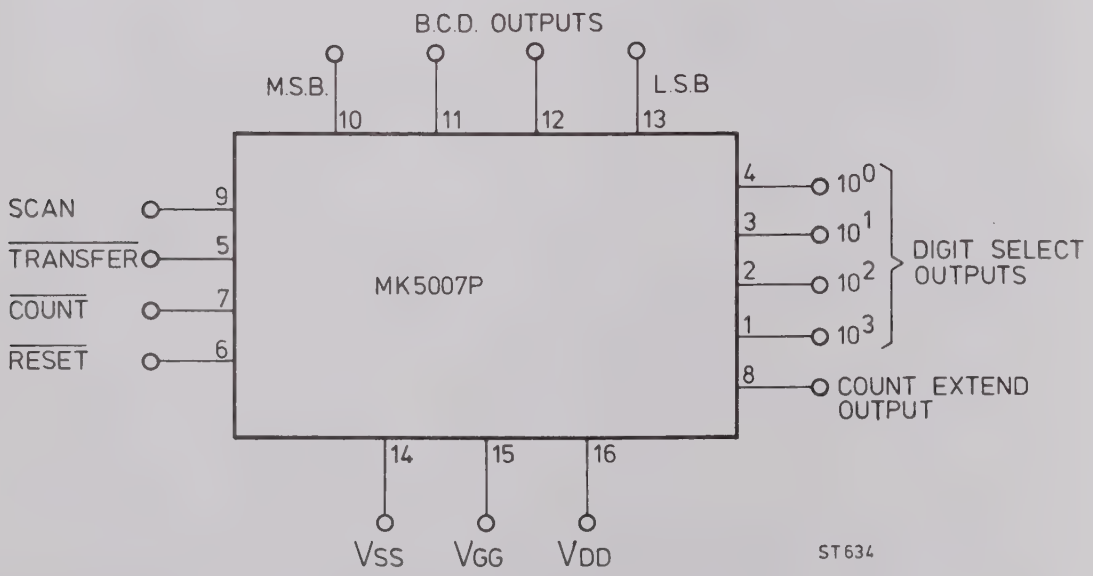


Fig. 25. Pin connections of MK 5007 P.

XI. List of parts

XI-1 MECHANICAL.

Item	Fig.	Qty.	Ordering number	Description
6	6	1	5322	Power switch
11	7	1	5322 277 24009	Slide switch
10	7	1		Power connector
8/9	6	2	5322 277 34021	Connector 2 pole
7	6	1	5322	Input connector red.
7	6	1	5322	Input connector black
13	7	1	5322 290 40065	Earth terminal
12	7	1	5322	Battery connector
BU 362	26	1	5322	Connector 7 pins
BU9	-	1	5322	Connector
BU11	24	1	5322	Connector 10 pole
BU12		1	5322	Connector
14	7	1	5322 256 34023	Fuse holder.
26	23	2	5322	Text plate
5	5	1	5322	Mode switch
	-	1	5322	Cap.
27	23	1	5322	Window
		4	5322 462 54072	Foot
17	23	2	5322 528 34089	Fixed part
18	23	2	5322 528 34091	Movable part
19	23	2	5322 492 64345	Wave spring
20	23	2	5322 462 44132	Cover
16	23	2	5322 502 64026	Shaft

Item	Fig.	Qty.	Ordering number	Description
22	23	1	5322 498 54037	Handle
		1	5322	Heat sink
BU362	24	1	5322	Connector 7 pole female
BU363	1-1	1	5322	Connector
BU364	24	1	5322	Connector
BU250/361	24	2	5322	Connector
		1	5322	Mains cable
15	7	1	5322	Current protector

XI-2 MISCELLANEOUS

Item	Fig.	Ordering number	Description
T1			Mains transformer
T359			Transformer
VL1			Mains fuse, 50mA, d.a.
B370	5	5322 131 94017	Display tube
B371	5	5322 131 94017	Display tube
B372	5	5322 131 94017	Display tube
B373	5	5322 131 94017	Display tube
LA1			Neon lamp
LA206		5322 134 24011	Neon lamp
LA365			Neon lamp
LA366			Neon lamp
LA367			Neon lamp
RE191		5322 280 24034	Reed switch
RE192		5322 280 24035	Reed switch
RE193		5322 280 24035	Reed switch
RE194		5322 280 24034	Reed switch
RE191*			Coil
RE192*			Coil
RE193*			Coil
RE194*			Coil

Item	Fig.	Qty.	Ordering number	Description
RE195				Relay
RE196				Relay
RE197				Relay
RE198				Relay
RE199				Relay
L201			5322 158 14059	Filter coil
L202			5322 158 14059	Filter coil
L353			5322 158 14059	Filter coil
L354			5322 158 14059	Filter coil
TS1	26			2SC 1014

XI-3 ELECTRICAL

XI-3.1 Resistors

Item	Ordering number	Value		%	Series	
R61		2	kΩ	0.1	1 W	Metal film
R62		2	kΩ	0.1	1 W	Metal film
R63		221.3	kΩ	0.1	0.25 W	Metal film
R64		20.16	kΩ	0.1	0.25 W	Metal film
R65		2	kΩ	20		Potentiometer
R66	5322 100 10138	100	kΩ	20		Potentiometer
R73	5322 116 50904	100	Ω	1	MR25	
R74	5322 116 54549	1	kΩ	1	MR25	
R75		9.2	kΩ	0.1	MR31C	
R76		9.2	kΩ	0.1	MR31C	
R77		200	Ω	20		Potentiometer
R78		2	kΩ	20		Potentiometer
R79	5322 101 14041	1	kΩ	20		Potentiometer
R86	5322 116 54619	10	kΩ	1	MR25	
R88		1	kΩ	0.1	MR31C	

No	Ordering number	Value	%	Series	
R90		1 kΩ	0.1	MR31C	
R98		30.1 kΩ	0.1	MR31C	
R99		19.1 kΩ	0.1	MR31C	
R100	5322 116 54006	392 kΩ	1	MR25	
R101	5322 116 54534	681 kΩ	1	MR25	
R102	5322 101 14037	500 Ω	20		Potentiometer
R107		38 MΩ	2		Metal glaze
R108		4.35 MΩ	0.5		Metal film
R109		404 kΩ	0.1		Metal film
R110		39.5 kΩ	0.1		Metal film
R111	5322 116 54513	332 Ω	1	MR25	
R115	5322 116 50527	33.2 Ω	1	MR25	
R116	5322 116 54087	39.2 Ω	1	MR25	
R117		500 kΩ	20		Potentiometer
R118		50 kΩ	20		Potentiometer
R119	5322 101 14037	500 Ω	20		Potentiometer
R120		50 Ω	20		Potentiometer
R129		9.9 MΩ	0.25	1W	Metal film
R130		9.9 MΩ	0.25	1W	Metal film
R131		99.45 kΩ	0.25	0.25W	Metal film
R132	322 101 14041	1 kΩ	20		Potentiometer
R137		90 Ω	0.1	0.25W	Metal film
R138					
R139		9 Ω	0.1	0.25W	Metal film
R140		1 Ω	0.1	0.25W	Metal film
R144	5322 116 54005	3.32 kΩ	1	MR25	
R145	5322 116 54005	3.32 kΩ	1	MR25	

No.	Ordering number	Value	%	Series	
R146		2 kΩ	20		Potentiometer
R147	5322 116 54011	5.62 kΩ	1	MR25	
R149		6x4,7 k		MR6-2	Composite
R152	5322 116 54525	511 Ω	1	MR25	
R153	5322 116 54615	9.09 kΩ	1	MR25	
R154	5322 116 54003	22.1 kΩ	1	MR25	
R155	5322 116 54615	9.09 kΩ	1	MR25	
R156	5322 116 54003	22.1 kΩ	1	MR25	
R158	5322 116 54007	475 Ω	1	MR25	
R159	5322 116 54038	221 kΩ	1	MR25	
R160	5322 116 54725	196 kΩ	1	MR25	
R161	5322 116 54007	475 Ω	1	MR25	
R162		200 Ω	20		Potentiometer
R163	5322 116 54486	150 Ω	1	MR25	
R164		19.8 kΩ	0.5	MR31C	
R165		20.0	0.5	MR31C	
R168		8.8 kΩ	0.25	0.25W	Metal film
R171		200 Ω	20		Potentiometer
R172		16.5 kΩ	0.25	MR31C	
R174		200 Ω	20		Potentiometer
R178		180 kΩ	0.5	MR31C	
R179		19.6 kΩ	0.25	MR31C	
R180		200 Ω	20		Potentiometer
R181	5322 116 54486	150 Ω	1	MR25	
R182	5322 116 54486	150 Ω	1	MR25	
R183	5322 116 54486	150 Ω	1	MR25	
R198	5322 101 14037	500 Ω	20		Potentiometer
R210	5322 101 14038	10 kΩ	20		Potentiometer
R211	5322 116 54676	56.2 kΩ	1	MR25	

No.	Ordering number	Value	%	Series	
R212	5322 116 54683	68.1 kΩ	1	MR25	
R214	5322 116 54683	68.1 kΩ	1	MR25	
R219	5322 116 54683	68.1 kΩ	1	MR25	
R223	5322 101 14042	20 kΩ	20		Potentiometer
R224	5322 116 54001	15 kΩ	1	MR25	
R286		6x4.7 kΩ		MR6-2	Composite
R287		6x4.7 kΩ		MR6-2	Composite
R288		6x4.7 kΩ		MR6-2	Composite
R289		10x47 kΩ		MR13-2	Composite

XI-3.2Capacitors

No.	Ordering number	Value	%	V	Description
C161		100 nF		400	Metal film
C163		47 pF	5	500	Mica
C165		430 pF	5	300	Mica
C166		7 pF	10	500	Mica
C167		430 pF	5	100	Mica
C168		6 pF		250	Var. Ceramic
C169		6 pF		250	Var. Ceramic
C170		10 µF	20	25	Electrolytic
C171		10 µF	20	25	Electrolytic
C172		10 µF	20	16	Electrolytic
C173		22 µF		16	Electrolytic
C174	5322 122 30002	150 pF	10	50	Ceramic
C175		1 µF		100	Film
C176		1 µF		100	Film
C177		330 nF		100	Film

No.	Ordering number	Value	%	V	Description
C178	5322 122 34021	47 nF	-0 +100	50	Ceramic
C179	5322 122 30101	220 pF	10	50	Ceramic
C180		4.7 nF		400	Polyester
C181		4.7 nF		400	Polyester
C182	5322 122 30101	220 pF	10	50	Ceramic
C183	5322 122 34021	47 nF	-0 +100	50	Ceramic
C184		10 µF	-0 +100	25	Electrolytic
C185		10 µF	-0 +100	25	Electrolytic
C186		22 µF		16	Electrolytic
C187		12 pF	-0 +100	50	Ceramic
C189		10 pF	10	300	Mica
C190		4 pF	0.5	500	Mica
C191	5322 122 31167	2.2 nF	-0 +100	50	Ceramic
C290	4822 124 20533	470 µF		25	Electrolytic
C291	4822 124 20533	470 µF		25	Electrolytic
C292	5322 122 34021	47 nF	-0 +100	50	Ceramic
C293	5322 122 30016	33 pF	10	50	Ceramic
C294	5322 122 31169	2.2 nF	-0 +100	50	Ceramic
C295		33 µF		16	Electrolytic
C296		33 µF		25	Electrolytic
C297	4822 124 20575	100 µF		25	Electrolytic
C298	4822 124 20575	100 µF		25	Electrolytic
C299		22 µF		50	Electrolytic
C300	4822 124 20488	100 µF		16	Electrolytic
C301		100 nF		200	Mylar
C302		1 µF		350	Electrolytic
C303		1 µF		160	Electrolytic
C304		1 nF		50	Mylar
C305	5322 122 34021	47 nF	-0 +100	50	Ceramic

No.	Ordering number	Value	%	V	Description
C306	5322 122 30016	33 pF	10	50	Ceramic
C308	5322 122 34021	47 nF	-0 +100	50	Ceramic
C309	5322 122 30016	33 pF	10	50	Ceramic
C310	5322 122 30016	33 pF	10	50	Ceramic
C311	5322 122 34021	47 nF	-0 +100	50	Ceramic
C312		4.7 µF		25	Electrolytic
C313		4.7 µF		25	Electrolytic
C314		220 nF	10	100	Mylar
C315	5322 122 30016	33 pF	10	50	Ceramic
C316	5322 122 34021	47 nF	-0 +100	50	Ceramic
C317		1 µF	10	100	Mylar
C318	5322 122 30027	1 nF	-0 +100	50	Ceramic
C319	5322 122 34021	47 nF	-0 +100	50	Ceramic
C320		2 pF	1	50	Ceramic
C321		6.8 µF		6.3	Electrolytic
C322		120 pF		100	Mica
C323		120 pF		100	Mica
C324	5322 122 30021	100 pF	10	50	Ceramic
C325		2.2 nF	10	50	Mylar
C326	5322 122 30027	1 nF	-0 +100	50	Ceramic
C327	5322 122 30021	100 pF	10	50	Ceramic
C328		4.7 nF	10	50	Mylar
C329	5322 122 30027	1 nF	-0 +100	50	Ceramic
C330	5322 122 30021	100 pF	10	50	Ceramic
C331		680 nF		35	Electrolytic
C332	5322 122 30027	1 nF	-0 +100	50	Ceramic
C334	5322 122 30021	100 pF	10	50	Ceramic
C335		2.2 µF	10	20	Electrolytic
C336	5322 122 30027	1 nF	-0 +100	50	Ceramic
C337	5322 122 30027	1 nF	-0 +100	50	Ceramic

No.	Ordering number	Value	%	V	Description
C338	5322 122 30027	1 nF	-0 +100	50	Ceramic
C339	5322 122 30027	1 nF	-0 +100	50	Ceramic
C341	5322 122 30129	4.7 nF	-0 +100	50	Ceramic
C342	5322 122 34021	47 nF	-0 +100	50	Ceramic
C343	5322 122 30129	4.7 nF	-0 +100	50	Ceramic
C344		10 nF	10	200	Mylar
C345		10 nF	10	200	Mylar
C346		10 nF	10	200	Mylar
C347		10 nF	10	200	Mylar
C348	5322 122 34021	47 nF	-0 +100	50	Ceramic
C349	5322 122 34021	47 nF	-0 +100	50	Ceramic
C350	5322 122 34021	47 nF	-0 +100	50	Ceramic
C352	5322 122 30027	1 nF	-0 +100	50	Ceramic
C356	5322 122 34021	47 nF	-0 +100	50	Ceramic
C355	5322 122 30027	1 nF	-0 +100	50	Ceramic
C371		330 pF	5	300	Mica
C372		70 pF			Trimmer

XI-3.3Transistors

No.	Ordering number	Type	
TS15		2SK-12 (Y)	Located on printed circuit board U1
TS16	5322 130 44168	2SC269	
TS17		IMF3958	
TS18		ITS30088	
TS20		2N4393	

No.	Ordering number	Type	
TS21		2N4393	Located on printed circuit board U1
TS22		2N4393	
TS23		2N4393	
TS24 to TS36	5322 130 44168	2SC269	
TS22	5322 130 44168	2SC269	
TS23	5322 130 44168	2SC269	
TS24		2SD220	
TS25		2SD220	
TS26		2SA495G	
TS27	5322 130 44168	2SC269	
TS28		ITS30088	
TS29		ITS30088	
TS30		IMF3958	
TS31 to TS34		2N4393	
TS35	5322 130 44166	ITS3948	
TS36 toTS38		2N4393	
TS39		IMF3955	
TS40		ITS30088	
TS41		2SA495G	
TS42	5322 130 44168	2SC269	
TS43	5322 130 44168	2SC269	
TS44		2SC268B	
TS45	5322 130 44168	2SC269	
TS46		2SC268B	
TS47	5322 130 44168	2SC269	
TS48		2SC268B	
TS49 to TS71	5322 130 44168	2SC269	
TS72 to TS84		2SC268B	
TS85 to TS87	5322 130 44169	2SC268A	

XI-3.4 Diodes

No.	Ordering number	Type	
GR1		F14-C	Located on printed circuit board U1.
GR41	5322 130 34216	WG713	
GR42	5322 130 34216	WG713	
GR43	5322 130 34199	1S953	
GR44	5322 130 34199	1S953	
GR45 to GR52	5322 130 34216	WG713	
GR53 to GR55		F14-C	
GR56		1S1544A	
GR93	5322 130 34204	1S1850R	
GR94	5322 130 34205	1S1850	
GR95		RD6A	Zener
GR96 to GR100	5322 130 34199	1S953	
GR101		F14-C	
GR102		F14-C	
GR103 to GR105		F14-E	
GR106	5322 130 34199	1S953	
GR107	5322 130 34199	1S953	
GR108		02Z7.5A	Zener
GR109		ZD-2	Zener
GR110	5322 130 34199	1S953	
GR111	5322 130 34199	1S953	
GR113 to GR121	5322 130 34199	1S953	
GR123 to GR131	5322 130 34199	1S953	
GR132 to GR135		F14-C	
GR136	5322 130 34199	1S953	

No.	Ordering number	Type
GR137		SLP20B
GR138 to GR140	5322 130 34199	1S953

XI-3.5 Integrated circuits

No.	Ordering number	Type	Manufacturer	
IC1		LM301A	National	Located on Printed wiring board U1
IC2		LM301A	"	
IC3		SN74L51	Texas Instr.	
IC4	5322 209 84193	SN74L02N	"	
IC5		SN74L03N	"	
IC6	5322 209 84196	SN74L73N	"	
IC7	5322 209 84189	SN74L04N	"	
IC8	5322 209 84187	SN74L00N	"	
IC9		SN74L10N	"	
IC10	5322 209 84196	SN74L73N	"	
IC11	5322 209 84187	SN74L00N	"	
IC12		SN74L03N	"	
IC1		LM301A	National	Printed on printed circuit board U2
IC2		LM301A	"	
IC3		LM709C	"	
IC4		LM301A	"	
IC5		LM301A	"	
IC6	5322 209 84241	LM311	"	
IC7	5322 209 84189	SN74LO4N	Texas Instr.	
IC8	5322 209 84187	SN74L00N	"	
IC9	5322 209 84187	SN74L00N	"	

No.	Ordering number	Type	Manufacturer	
IC11	5322 209 84191	SN74L74N	Texas Instr.	Located on printed circuit board U2
IC12		SN74L10N	"	
IC13	5322 209 84196	SN74L73N	"	
IC14	5322 209 84191	SN74L74N	"	
IC15		MK5007P		
IC16	5322 209 80141	SN7414IN	Texas Instr.	

XI-4 OPTIONAL ACCESSORY PM 9207

No.	Ordering number	Type	
TS1	5322 130 44168	2SC269	
TS2		2SA495G	
TS3	5322 130 44168	2SC269	
GR5		F14-C	
GR6		F14-C	
GR7		RD6A	Zener
R25		2 kΩ 20%	Potentiometer
C31		470 µF 35V	Electrolytic
C32	5322 122 30027	1 nF 50V	Ceramic

This parts list does not contain multi-purpose and standard parts. These components are indicated in the circuit diagram by means of identification marks. The specification can be derived from the survey below.

Diese Ersatzteilliste enthält keine Universal- und Standard-Teile. Diese sind im jeweiligen Prinzipschaltbild mit Kennzeichnungen versehen. Die Spezifikation kann aus nachstehender Übersicht abgeleitet werden.

In deze stuklijst zijn geen universele en standaardonderdelen opgenomen. Deze componenten zijn in het principeschema met een merkteken aangegeven. De specificatie van deze merktekens is hieronder vermeld.

La présente liste ne contient pas des pièces universelles et standard. Celles-ci ont été repérées dans le schéma de principe. Leurs specifications sont indiquées ci-dessous.

Esta lista de componentes no comprende componentes universales ni standard. Estos componentes están provistos en el esquema de principio de una marca. El significado de estas marcas se indica a continuación.

Carbon resistor E24 series
Kohleschichtwiderstand, Reihe E24
Koolweerstand E24 reeks
Résistance au carbone, série E24
Resistencia de carbón, serie E24

0,125 W 5%

Carbon resistor E12 series
Kohleschichtwiderstand, Reihe E12
Koolweerstand E12 reeks
Résistance au carbone, série E12
Resistencia de carbón, serie E12

1 W ≦ 2,2 MΩ, 5%
> 2,2 MΩ, 10%

Carbon resistor E12 series
Kohleschichtwiderstand, Reihe E12
Koolweerstand E12 reeks
Résistance au carbone, série E12
Resistencia de carbón, serie E12

0,25 W ≦ 1 MΩ, 5%
> 1 MΩ, 10%

Carbon resistor E12 series
Kohleschichtwiderstand, Reihe E12
Koolweerstand E12 reeks
Résistance au carbone, série E12
Resistencia de carbón, serie E12

2 W 5%

Carbon resistor E24 series
Kohleschichtwiderstand, Reihe E24
Koolweerstand E24 reeks
Résistance au carbone, série E24
Resistencia de carbón, serie E24

0,5 W ≦ 5 MΩ, 1%
> 5 ≦ 10 MΩ, 2%
> 10 MΩ, 5%

Wire-wound resistor
Drahtwiderstand
Draadgewonden weerstand
Résistance bobinée
Resistencia bobinada

0,4 – 1,8 W 0,5%

Carbon resistor E12 series
Kohleschichtwiderstand, Reihe E12
Koolweerstand E12 reeks
Résistance au carbone, série E12
Resistencia de carbón, serie E12

0,5 W ≦ 1,5 MΩ, 5%
> 1,5 MΩ, 10%

Wire-wound resistor
Drahtwiderstand
Draadgewonden weerstand
Résistance bobinée
Resistencia bobinada

5,5 W ≦ 200 Ω, 10%
> 200 Ω, 5%

Wire-wound resistor
Drahtwiderstand
Draadgewonden weerstand
Résistance bobinée
Resistencia bobinada

10 W 5%

Tubular ceramic capacitor
Rohrkondensator
Keramische kondensator, buistype
Condensateur céramique tubulaire
Condensador cerámico tubular

500 V

Polyester capacitor
Polyesterkondensator
Polyesterkondensator
Condensateur au polyester
Condensador polyester

400 V

Tubular ceramic capacitor
Rohrkondensator
Keramische kondensator, buistype
Condensateur céramique tubulaire
Condensador cerámico tubular

700 V

Flat-foil polyester capacitor
Miniatur-Polyesterkondensator (flach)
Platte miniatuur polyesterkondensator
Condensateur au polyester, type plat
Condensador polyester, tipo de placas planas

250 V

Ceramic capacitor, "pin-up"
Keramikkondensator "Pin-up" (Perltyp)
Keramische kondensator "Pin-up" type
Condensateur céramique, type perle
Condensador cerámico, versión "colgable"

500 V

Paper capacitor
Papierkondensator
Papierkondensator
Condensateur au papier
Condensador de papel

1000 V

"Microplate" ceramic capacitor
Miniatur-Scheibenkondensator
"Microplate" keramische kondensator
Condensateur céramique "microplate"
Condensador cerámico "microplaca"

30 V

Wire-wound trimmer
Drahttrimmer
Draadgewonden trimmer
Trimmer à fil
Trimmer bobinado

Mica capacitor
Glimmerkondensator
Micakondensator
Condensateur au mica
Condensador de mica

500 V

Tubular ceramic trimmer
Rohrtrimmer
Buisvormige keramische trimmer
Trimmer céramique tubulaire
Trimmer cerámico tubular

For multi-purpose and standard parts, please see PHILIPS' Service Catalogue.

Für die Universal- und Standard-Teile siehe den PHILIPS Service-Katalog.

Voor universele en standaardonderdelen raadplege men de PHILIPS Service Catalogus.

Pour les pièces universelles et standard veuillez consulter le Catalogue Service PHILIPS.

Para piezas universales y standard consulte el Catálogo de Servicio PHILIPS.

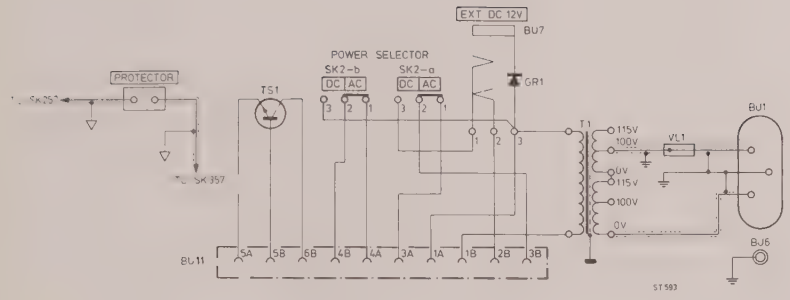


Fig. 26. Rear panel wiring

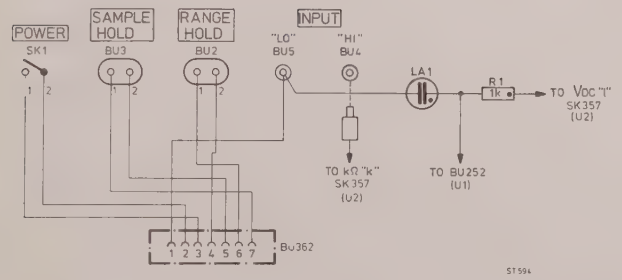


Fig. 28. Right-side wiring

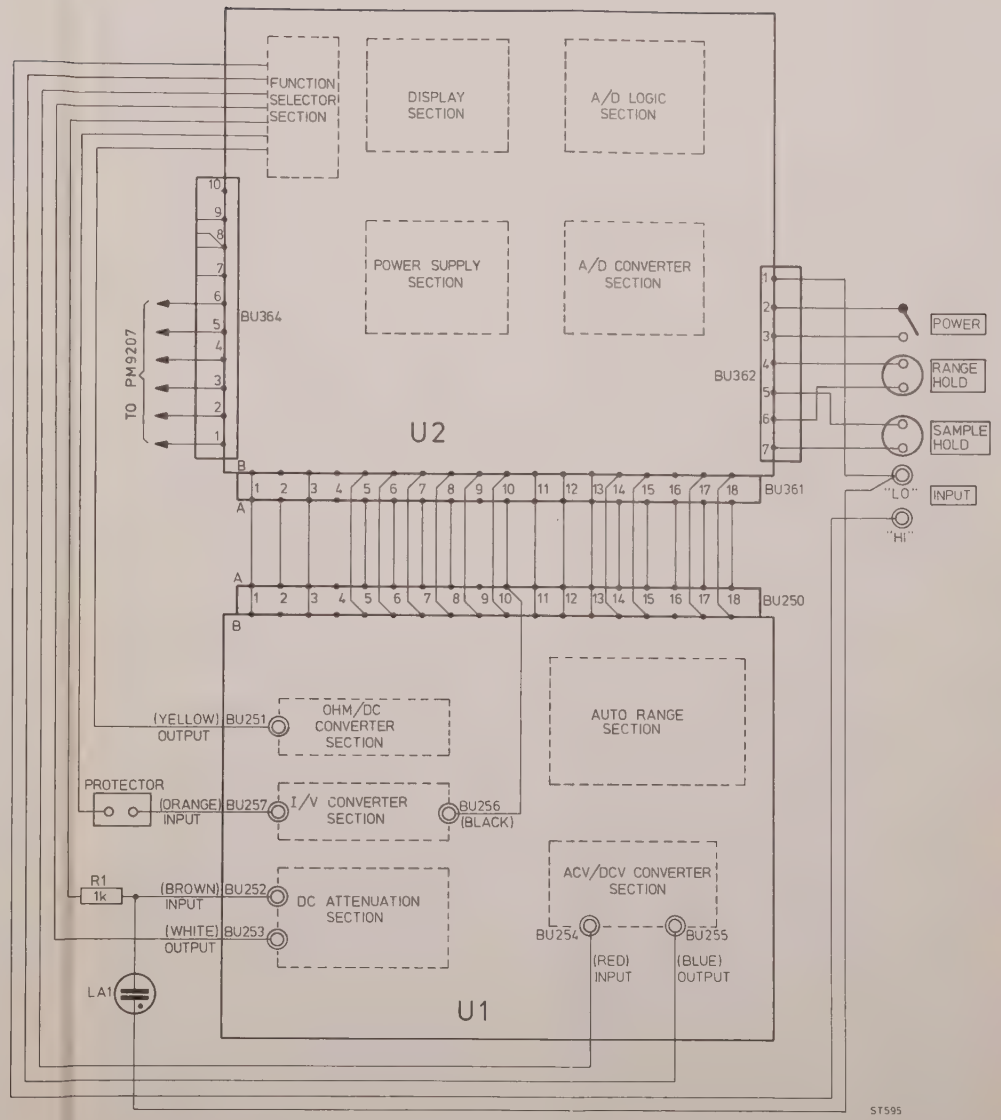


Fig. 29. Wiring diagram board to board

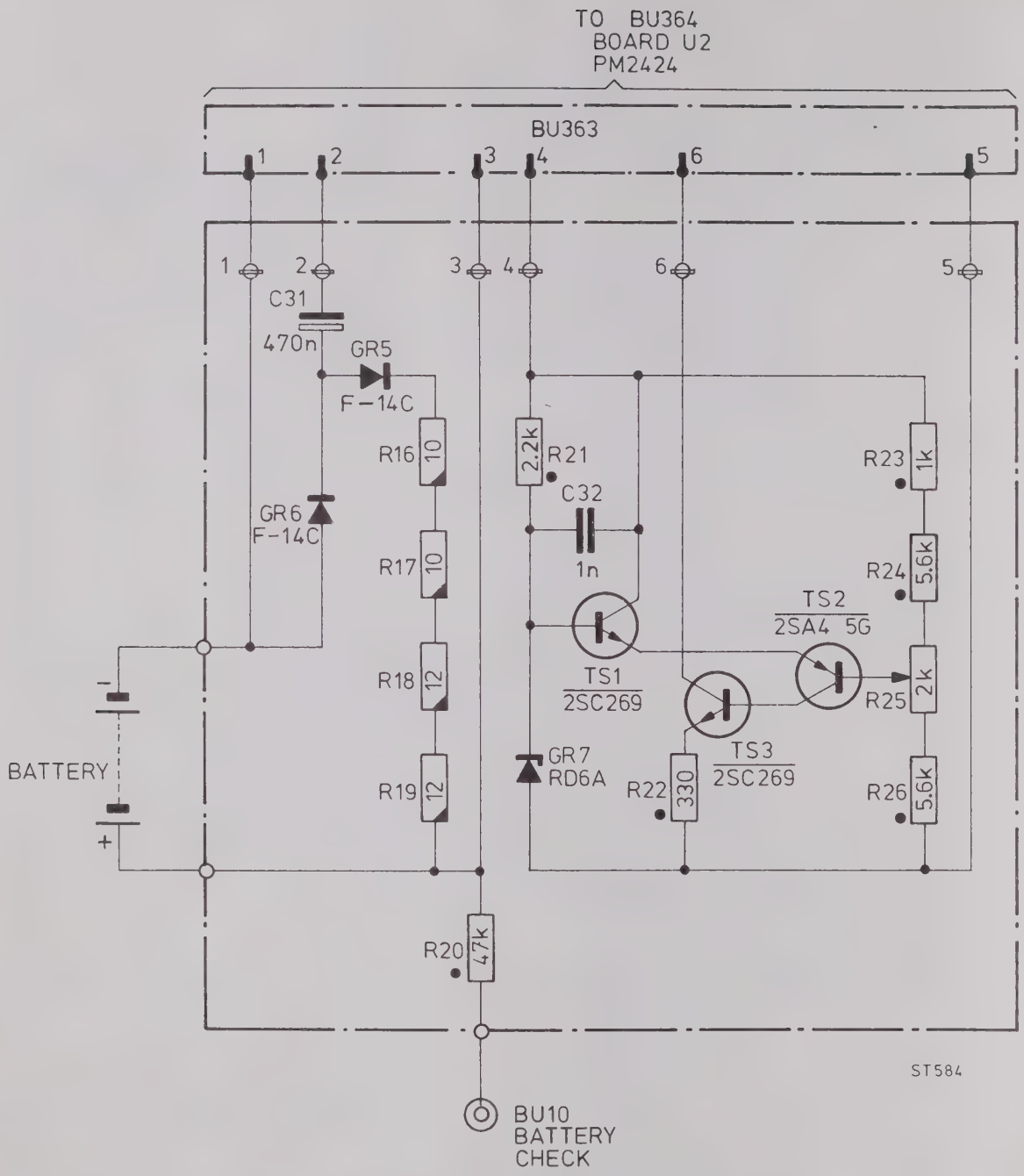


Fig. 30. Circuit diagram of optional accessory PM 9207

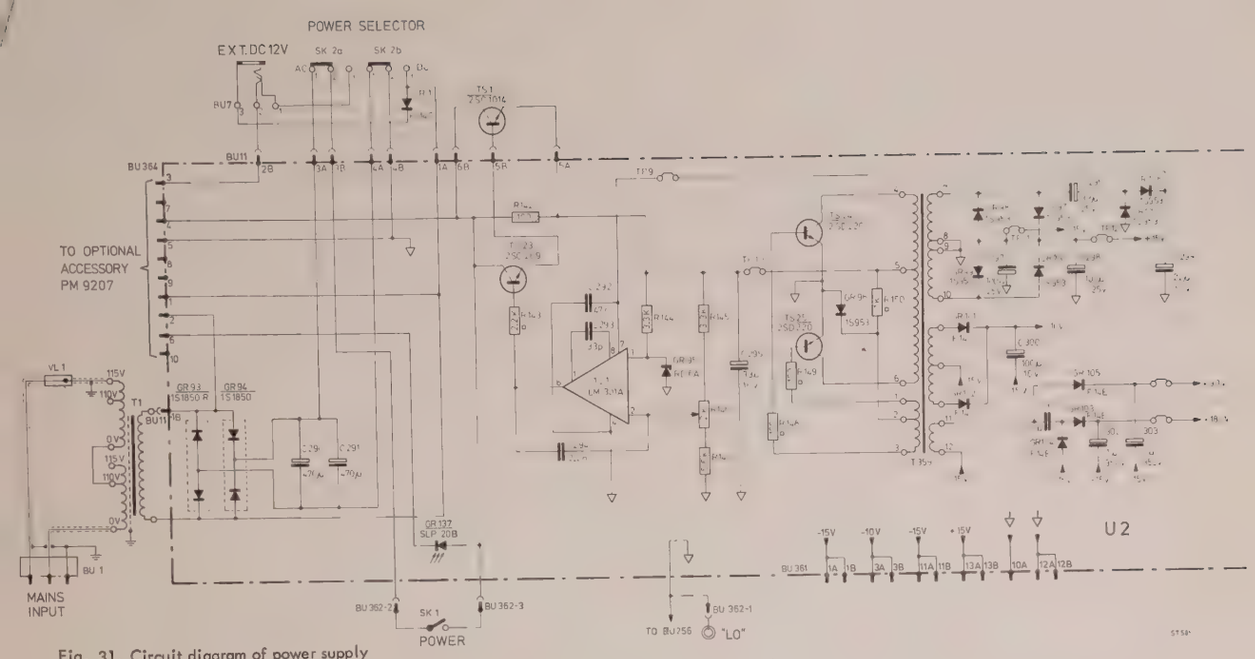


Fig. 31. Circuit diagram of power supply

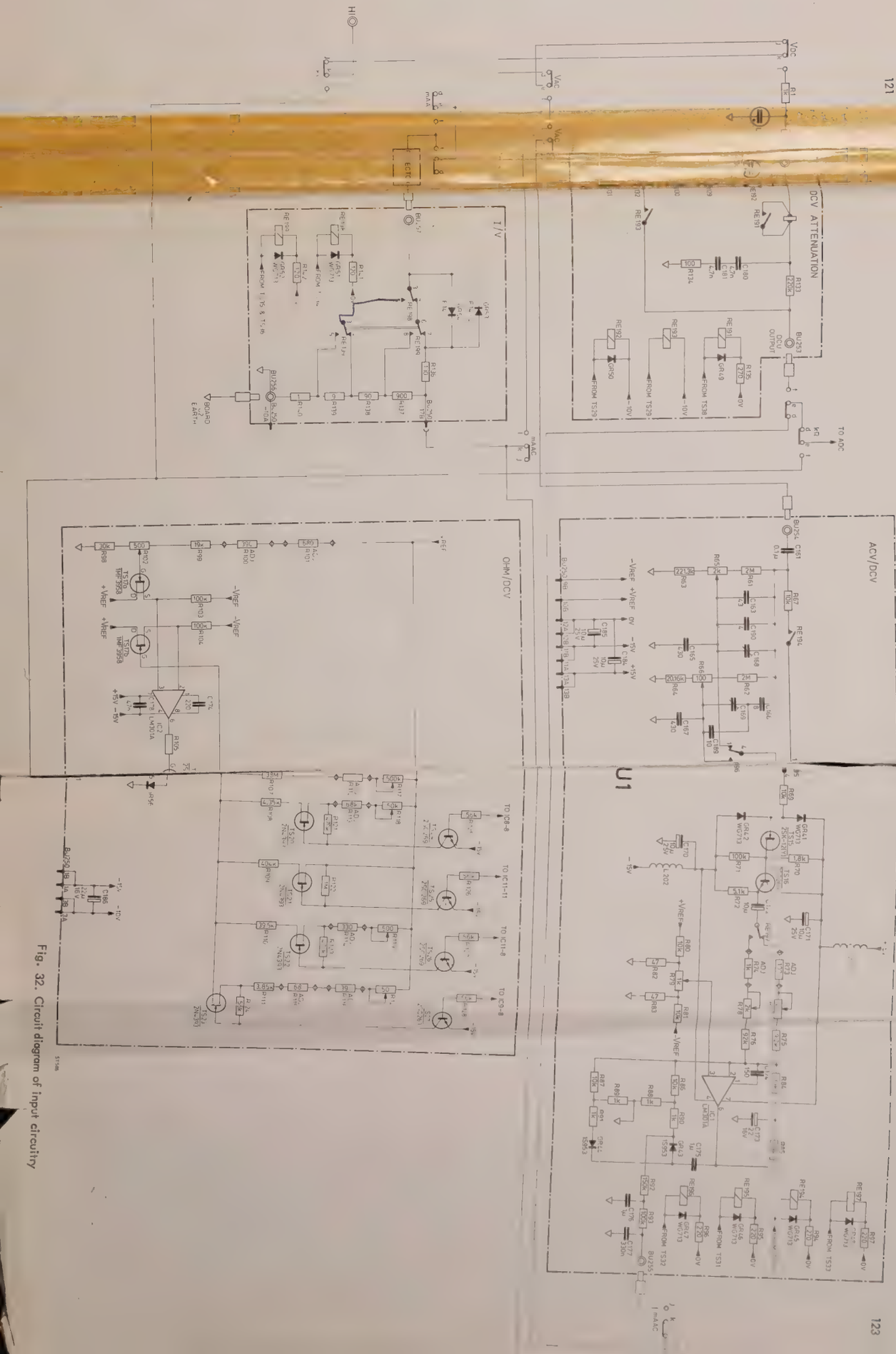


Fig. 32. Circuit diagram of input circuitry

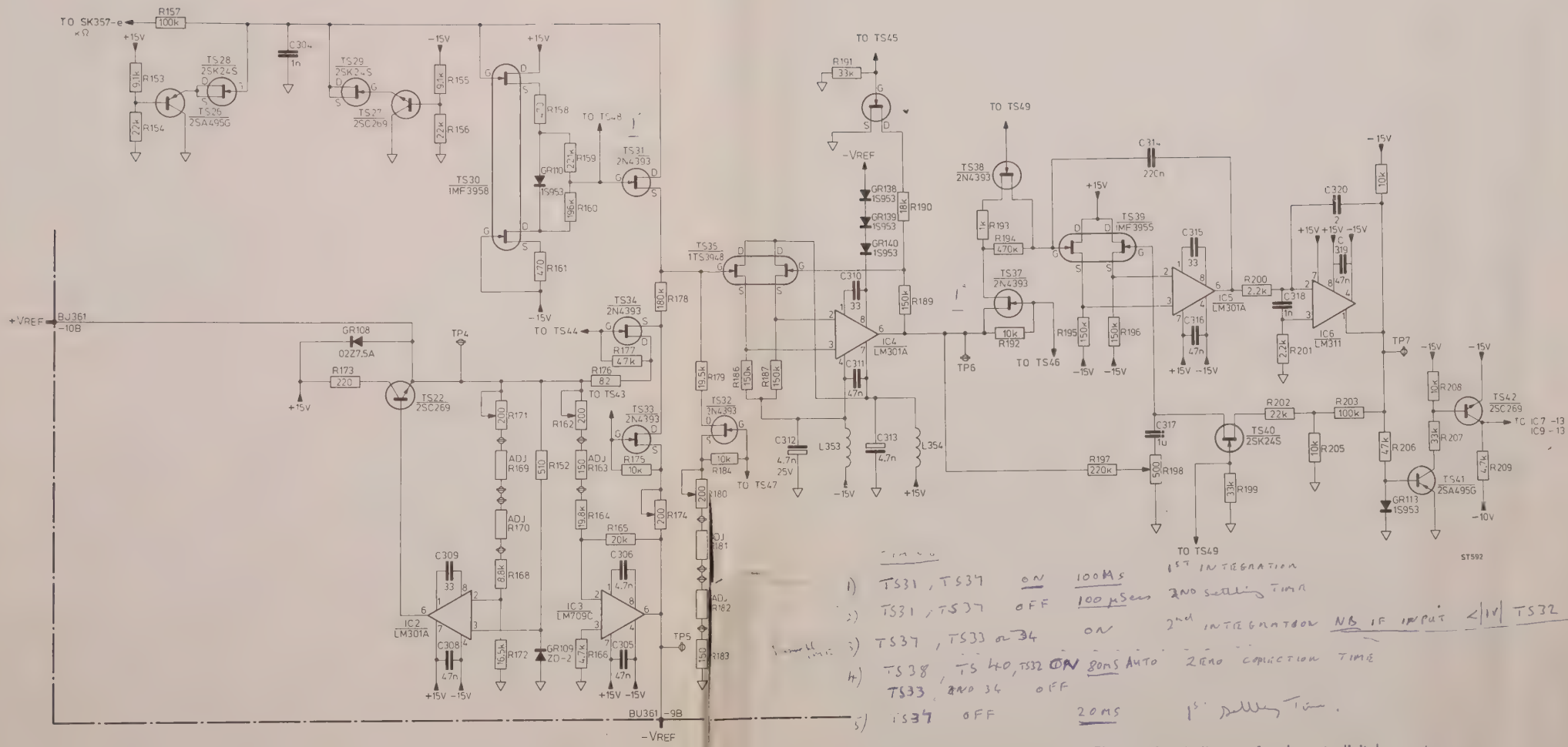


Fig. 33. Circuit diagram of analogue-to-digital converter

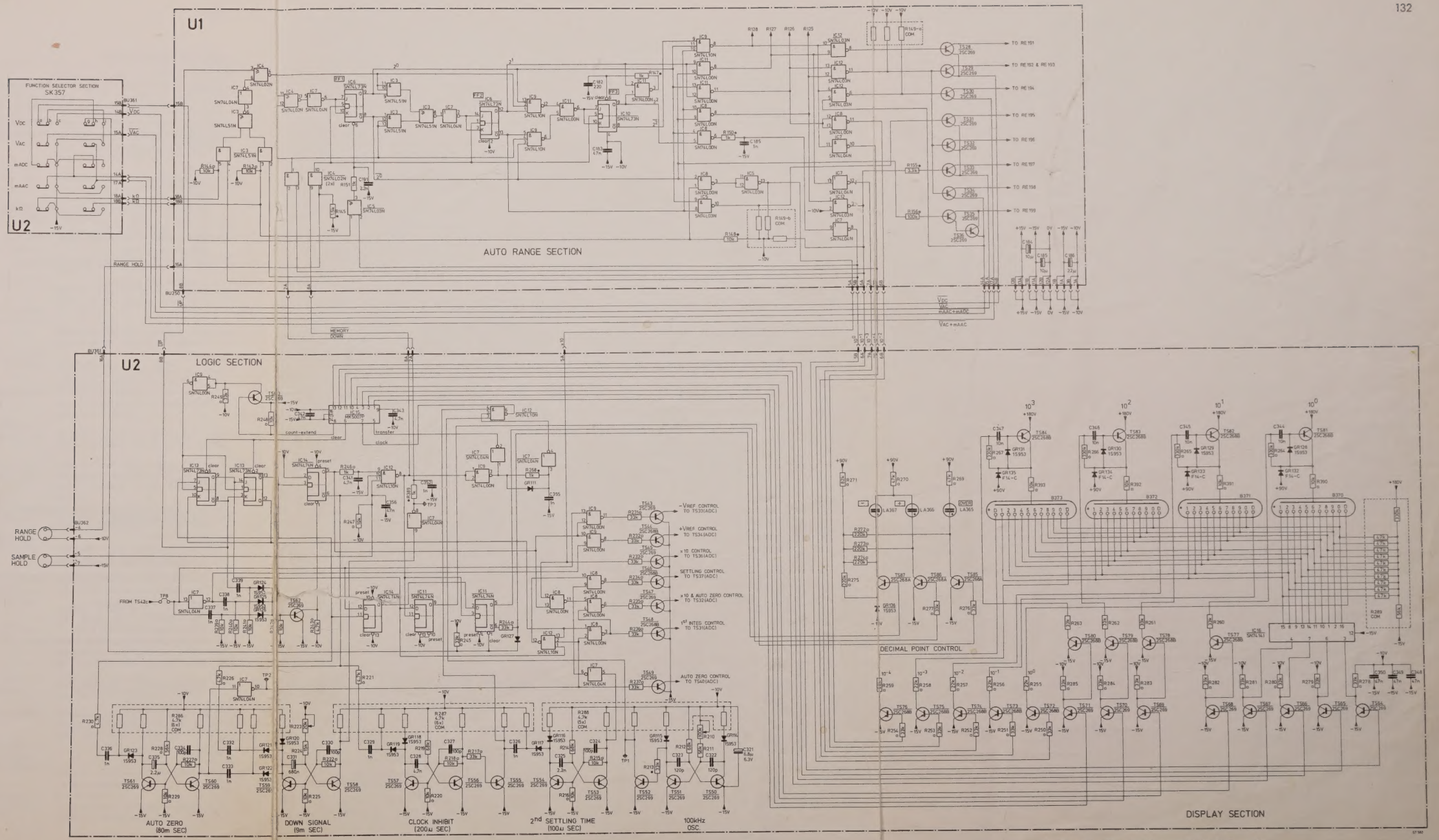


Fig. 34. Circuit diagram of digital section

QUALITY REPORTING

CODING SYSTEM FOR FAILURE DESCRIPTION

The following information is meant for Philips service workshops only and serves as a guide for exact reporting of service repairs and maintenance routines on the workshop charts.
For full details reference is made to Information G1 (Introduction) and Information Cd 689 (Specific information for Test and Measuring Instruments).

LOCATION

□□□

Unit number
e.g. 000A or 0001 (for unit A or 1; not 00UA or 00U1)

or: Type number of an accessory (only if delivered with the equipment)
e.g. 9051 or 9532 (for PM 9051 or PM 9532)

or: Unknown/Not applicable
0000

CATEGORY

□

0 Unknown, not applicable (fault not present, intermittent or disappeared)
1 Software error
2 Readjustment
3 Electrical repair (wiring, solder joint, etc.)
4 Mechanical repair (polishing, filing, remachining, etc.)
5 Replacement
6 Cleaning and/or lubrication
7 Operator error
8 Missing items (on pre-sale test)
9 Environmental requirements are not met

COMPONENT/SEQUENCE NUMBER

□□□□

Enter the identification as used in the circuit diagram, e.g.:

GR1003	Diode GR1003
TS0023	Transistor TS23
IC0101	Integrated circuit IC101
R0....	Resistor, potentiometer
C0....	Capacitor, variable capacitor
B0....	Tube, valve
LA....	Lamp
VL...	Fuse
SK....	Switch
BU....	Connector, socket, terminal
T0....	Transformer
L0....	Coil
X0....	Crystal
CB....	Circuit block
RE....	Relay
ME....	Meter, indicator
BA....	Battery
TR....	Chopper

Parts not identified in the circuit diagram:

990000	Unknown/Not applicable
990001	Cabinet or rack (text plate, emblem, grip, rail, graticule, etc.)
990002	Knob (incl. dial knob, cap, etc.)
990003	Probe (only if attached to instrument)
990004	Leads and associated plugs
990005	Holder (valve, transistor, fuse, board, etc.)
990006	Complete unit (p.w. board, h.t. unit, etc.)
990007	Accessory (only those without type number)
990008	Documentation (manual, supplement, etc.)
990009	Foreign object
990099	Miscellaneous

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